

*Project*

**ACN - HIFAS**

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# Californium Radiation Test Report

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# 1 Introduction

## 1.1 Background

As the HIFAS chip has been produced in a commercial process, we have little knowledge on the radiation sensitivity of the device. One of the major effects of radiation on digital circuits is single-event upsets, where heavy particle radiation into the chip can cause bit flips in the circuits.

As the chip has been optimised for power consumption and performance, there is no dedicated SEU handling built-in. However, there is redundancy in the measured data making it possible to correct single-bit errors, as long as the error rate is at a reasonable level.

ESA has a radiation test facility called CASE, which uses a radioactive Californium source to provide the radiation. While Californium is not representative of the radiation the chip would get in a space environment, this gives an order-of-magnitude figure of the SEU sensitivity of the device.

## 1.2 Purpose

This document describes the SEU testing on the HIFAS chip performed using ESTEC:s Californium radiation facilities.

The purpose of these tests is:

- Getting a figure on the SEU radiation sensitivity of the device.
- Verifying that the impact of an SEU on the device matches with the predictions.
- Evaluating the test hardware and software

## 1.3 Scope

Due to the limitations of testing with Californium, the figures obtained will be very approximative.

These tests do not cover other radiation effects such as the total dose effect, which are to be tested separately.

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## 2 ASIC Overview

### 2.1 Application

The HIFAS ASIC is a signal processing chip which samples a signal pair (called I and Q) with three-level precision, then calculates digitally the auto- and cross-correlations with time delays from 0 to 512. This data can then be read out post-processed to produce a power spectrum of the measured signal.

The read-out data consists of a time counter, “monitors” counting the number of high and low samples, and correlation products II,IQ,QI,QQ with time lags between 0 and 511.

The chip can be used in two modes. In complex mode, the input should be a complex I,Q signal pair and the output data becomes the complex autocorrelation function. In real mode, the input should be a single power-splitted IF signal and the output data becomes the real-valued autocorrelation function.

### 2.2 Circuit parts

The chip consists of the following parts, listed in order of the signal chain:

- 3-level samplers with x4 time demultiplexing (bipolar).
- Data path and delay lines
- Multipliers
- 8-bit fast counters and 4-by-4 joining of multiplexed products
- 2 x 31-bit wide positive and negative slow counters
- Readout logic
- I/O buffers and pads

### 2.3 SEU impact

A complete SEU failure mode analysis of the chip is beyond the scope of this document. However, looking at the design at a higher level, one can observe the following.

- An SEU in the signal chain before the fast counters can propagate to the lowest bit of the fast counters, causing at most a change of +/- 1 count in the corresponding read-out data values.
  - An SEU in the fast or slow counters will cause a corresponding change in the read-out data value.
  - An SEU in the readout logic can cause a corrupted data read-out. However, since the readout logic is held in (asynchronous) reset during integration, this happens only if the SEU occurs during read-out.
-

## 2.4 System-level considerations

There are some important points regarding this chip that need to be taken into account:

- The signals processed by the ASIC are very noisy radiometric signals, and the output data is also random in nature. We can therefore accept small random errors in the lower bits in the read-out data and regard this as a small additional noise contribution.
- There is no controller state machine or similar in the chip. All state is held by the integrators and free-running buffers and counters. There is no “invalid” state where the chip does not run.
- The integration counters are reset between each measurement so a single event in those can affect at most one single measurement.
- The read-out logic is held reset during each measurement so a single event can affect at most one read-out.

## 2.5 SEU Error Detection and Correction Methods

Described in this section are some methods for detecting and/or correcting errors. Most of them rely on SEU detection by changes or differences that are much larger than the noise level in the data. This must be determined on individual instrument and observation level and depend on clock rate, integration time and resolution as well as observation strategy. To outline a general strategy is not realistic but it is straightforward to simulate for different applications, based on assumed spectra and operational set-up.

The requirements for different application range from dynamic range of signals observed that are larger than receiver noise to signals that are  $10^6$  lower. The integration times can vary between 5 ms up to 30 s per data sample. A proposed method would be to take several simulated spectra - fourier transform these to get autocorrelation sets – apply different levels of bit errors – fourier transform back and look at results.

However, we can make a simplified analyses to get an idea about the order of magnitude.

For each bit we can compare the probability of an SEU in that bit against the probability of getting this difference (1,2,4,8,... counts) "naturally" through noise.

Assume we have in each measured correlation value a standard deviation of 5 sample counts for each measurement. Also let's say that we have an SEU probability in this particular register of 0.001%.

Calculating with the normal distribution, see equation below, we get that:

An error of 0.5-1.5 counts (+/- 0.1-0.3 x stdev) has probability 16%

An error of 1.5-2.5 counts (+/- 0.3-0.5 x stdev) has prob. 15%

An error of 3.5-4.5 counts has prob. 12%

An error of 7.5-8.5 counts has prob. 4%

An error of 15.5-16.5 counts has 0.1%

An error of 31.5-32.5 counts has 0.00000002%

$$y = \operatorname{erf}\left(\frac{32.5}{5 \times \sqrt{2}}\right) - \operatorname{erf}\left(\frac{31.5}{5 \times \sqrt{2}}\right)$$

So in this example the 5 lowest bits are more likely to be "flipped" by noise than by an SEU. The remaining 27 bits can be SEU corrected with low risk of false positives.

How much does it increase the noise level?

$0.001\% * 8 + 0.001\% * 4 + 0.001\% * 2 + 0.001\% * 1 = 0.015$  counts average error contribution, only a small fraction of the standard deviation.

A lot of this also depends on system issues, as mentioned above.

### 2.5.1 Data consistency check

There are some sanity checks that can be performed on the data.

The read-out time count should follow the formula  $t_c = f_{\text{samp}} t_{\text{int}} / 256$ , where  $f_{\text{samp}}$  is the sample rate (Hz) and  $t_{\text{int}}$  is the integration time (s). If there is a large deviation from this, there is something wrong with either the data or with the sample clock.

The sum of the low and high monitor value of I and Q must be equal to the corresponding zero-lag autocorrelation,  $II[0]$  or  $QQ[0]$ . This sum must also be less than the time count, and more than all other correlation values.

### 2.5.2 Multiple read-outs

It is possible to perform read-out of the same measurement multiple times. By doing this and comparing the data sets, it is possible to both detect and correct SEU errors in the read-out logic.

### 2.5.3 Equivalent data pairs

In the read-out data, there are pairs of values that are equivalent from a signal processing standpoint.

In complex mode, we have the pairs  $II[n] \sim QQ[n]$  and  $IQ[n] \sim -QI[n]$ .

In real mode, we have the pairs  $II[n] \sim QQ[n]$  and  $IQ[n] \sim QI[n+1]$ .

By checking the difference in these pairs and comparing against a threshold (determined by either simulation, testing or analysis), we can detect errors that are more likely to be caused by error than by natural variations.

When an error is detected, it can be handled by replacing the bad value with the other value in the pair. To determine which value is correct, we can see which of the two values that is closest to the neighbouring correlation lags.

### 2.5.4 Time series

In applications where the same signal is observed over and over, we can use the time dimension of the data to both check for errors (large jumps in the data) and correct errors by interpolation.

### 2.5.5 Correlation series

For noise source observations, the dynamics between neighboring time lags will be limited. A large change from one lag to another indicate an SEU triggered error.

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## 3 Test Setup

### 3.1 Hardware Overview

The test setup consists of the HIFAS ASIC mounted on a test board and placed inside an evacuated bell jar with the Californium source. Power to the test board is supplied through IDC connectors from a linear regulator board on the outside of the jar, which in turn is fed from a lab power supply.

The linear regulator board also contains current sensing circuits and an overcurrent protection function. The overcurrent latch status output and reset input signals are connected to the control PC through a USB-to-GPIO interface.

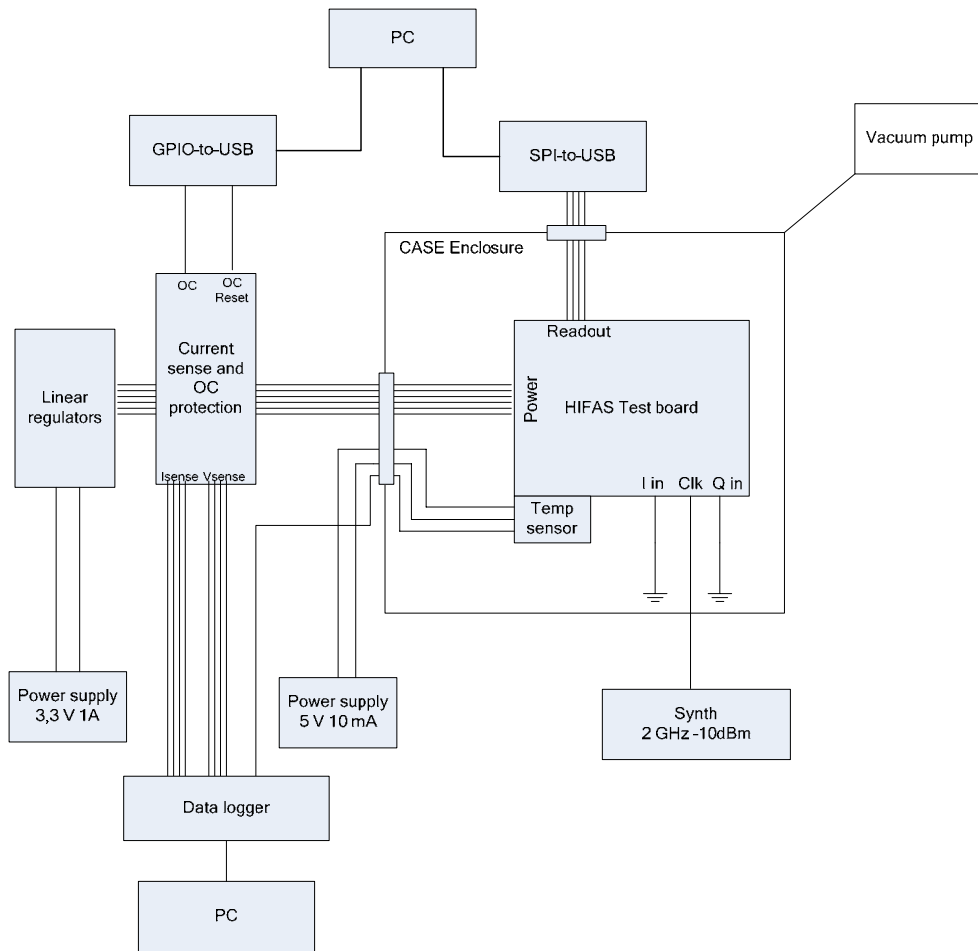
Sample clock to the test board is supplied from a synthesizer.

Data readout and control is handled by the control PC connected through an USB-to-SPI adapter, via an IDC connector, to the test board.

The supply rails and the current sensor outputs are fed to a data logger, controlled from a separate PC.

A temperature sensor is placed underneath the test board heat sink. It's powered through a power supply and the output signal goes to the data logger.

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**Figure 1. Block diagram of SEU test setup**

A photo of the different parts of the test setup are shown below. Of these, only the test board was inside the CASE system.



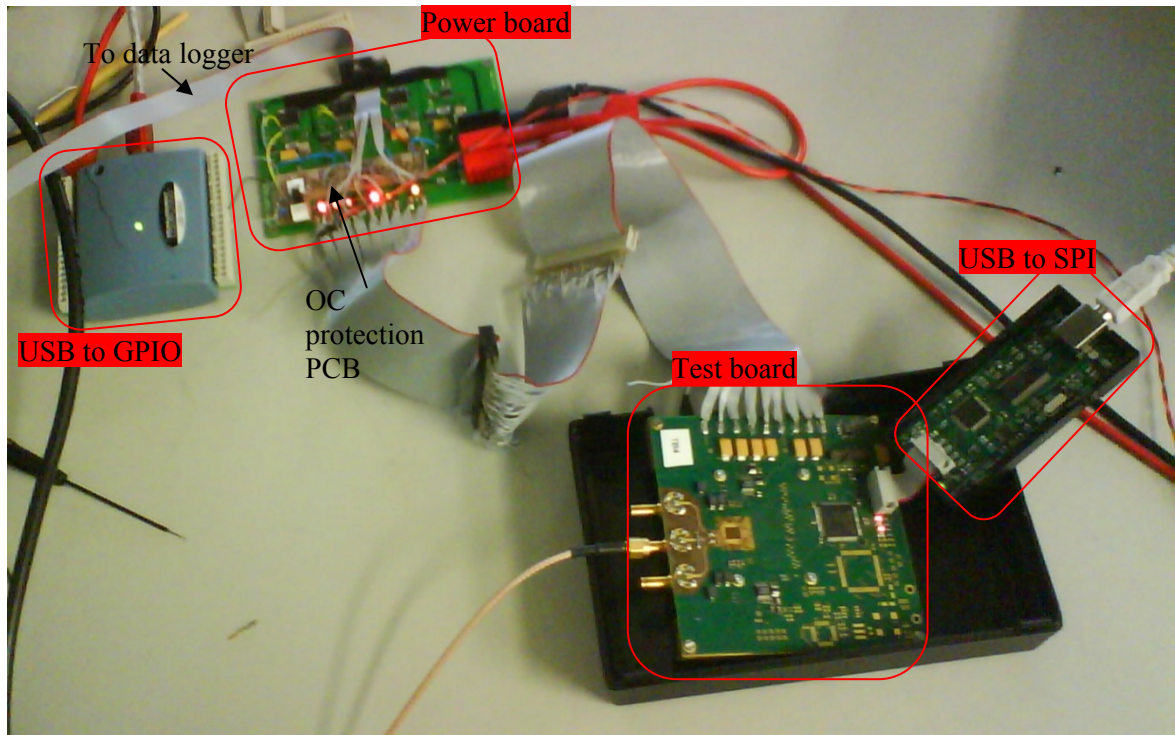


Figure 2. Photo of parts. Note that the USB-to-SPI interface is on the outside of the CASE system, connected through a 20x2 IDC cable (not shown)

## 3.2 ASIC Test board

The HIFAS test board was the same one as has been used for the performance tests (see note ACNTN3B), with two modifications:

- A temperature sensor has been added on the bottom side of the heat sink, directly underneath the ASIC.
- Power and data readout cables have been changed to fit the CASE system's 40-pin IDC interfaces.

A photo and block diagram of the board is shown below.

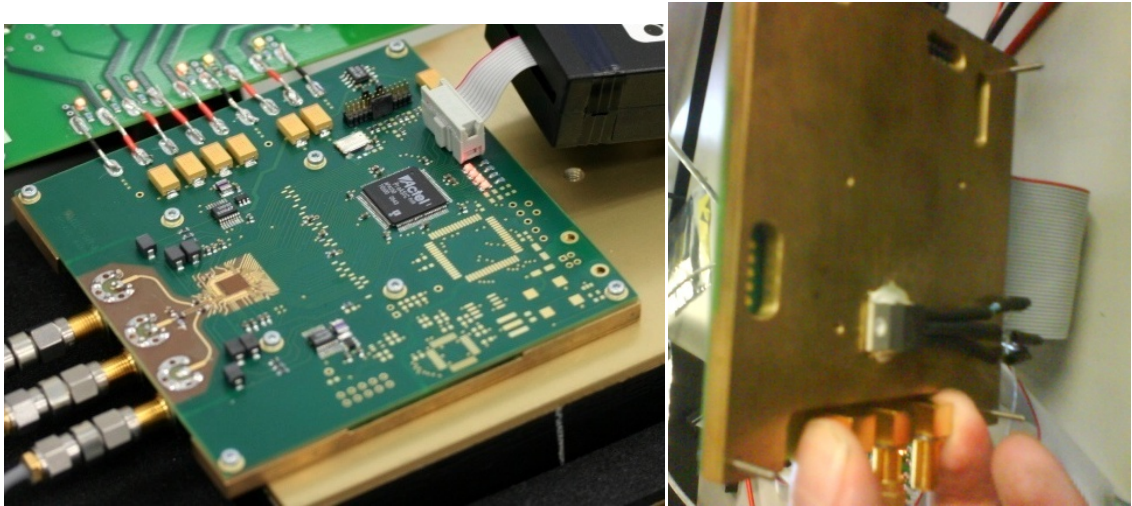


Figure 3. Left: Photo of test board (before modifications), Right: Temperature sensor on bottom side

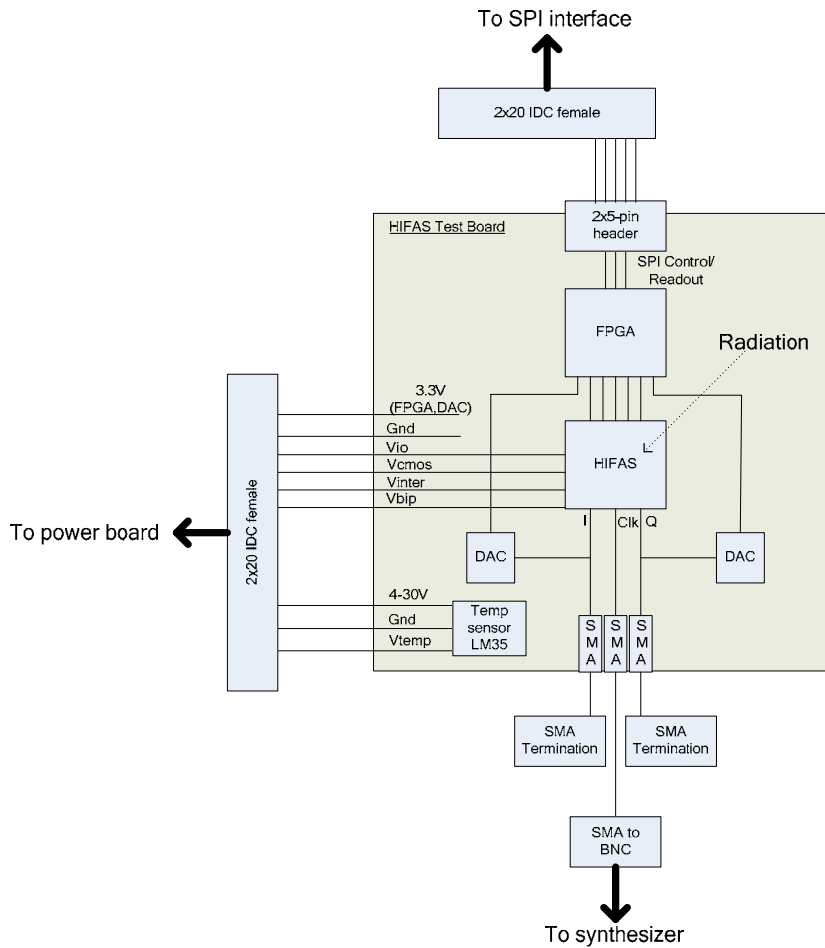


Figure 4. Simplified schematic of test setup inside the CASE system

### 3.3 Power board

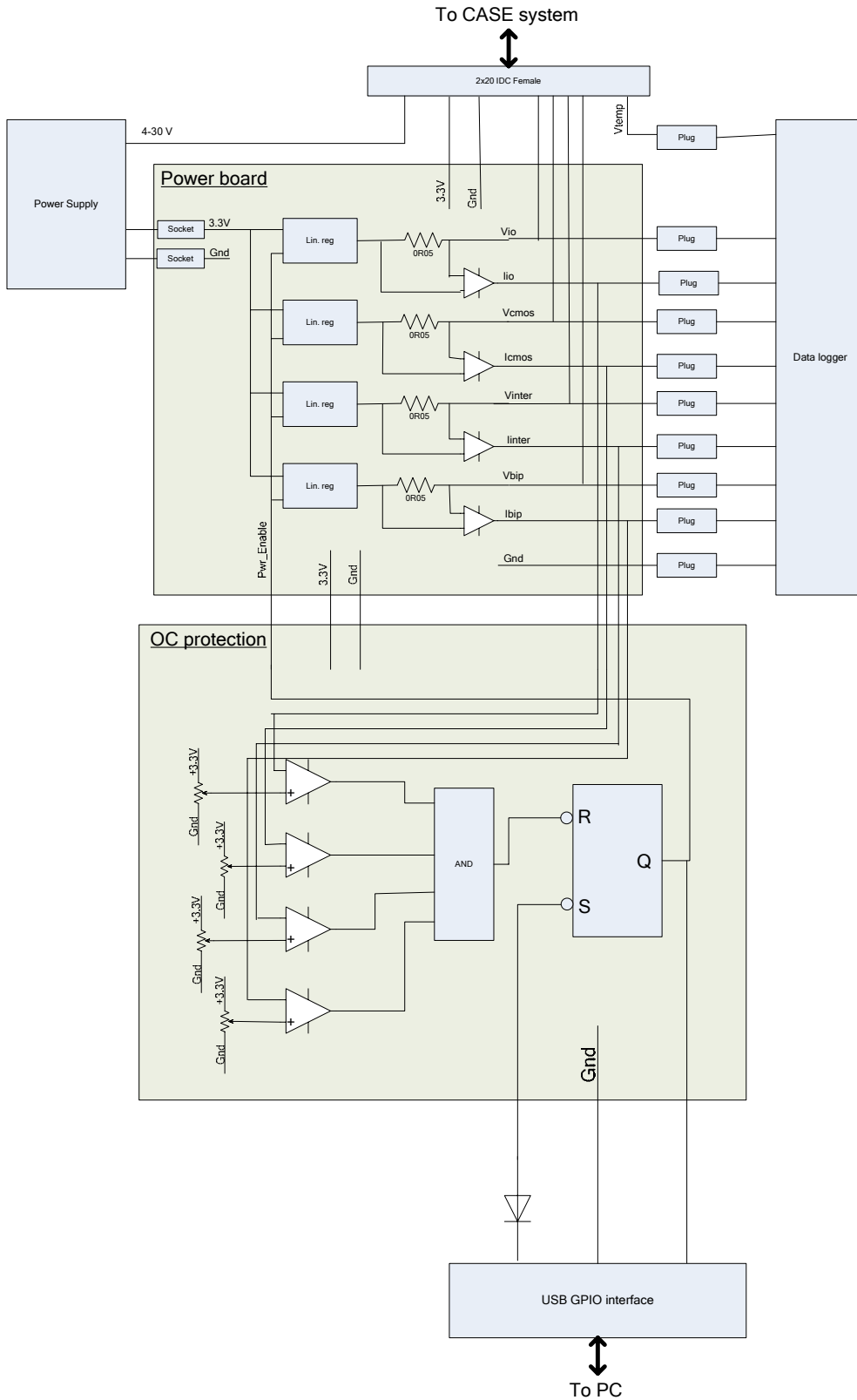


Figure 5. Simplified schematic of power board

### 3.4 CASE

The CASE system consists of a vacuum jar and the Californium source, as shown in the figure below. More information on the CASE system can be found at the ESCIES website, <https://escies.org/ReadArticle?docId=252>

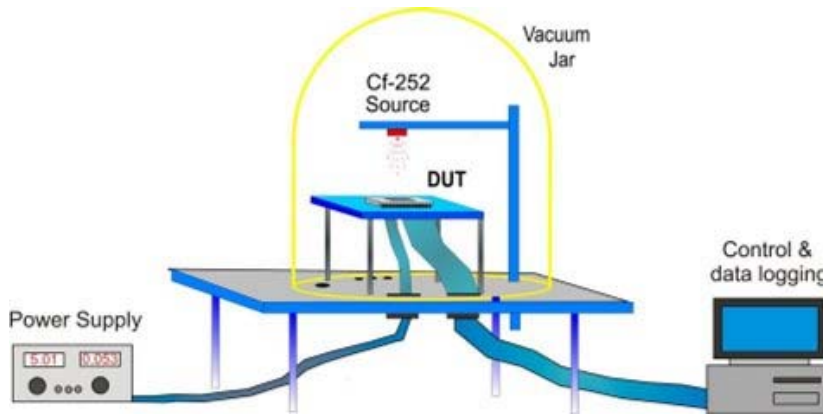


Figure 6. ESTEC Case system

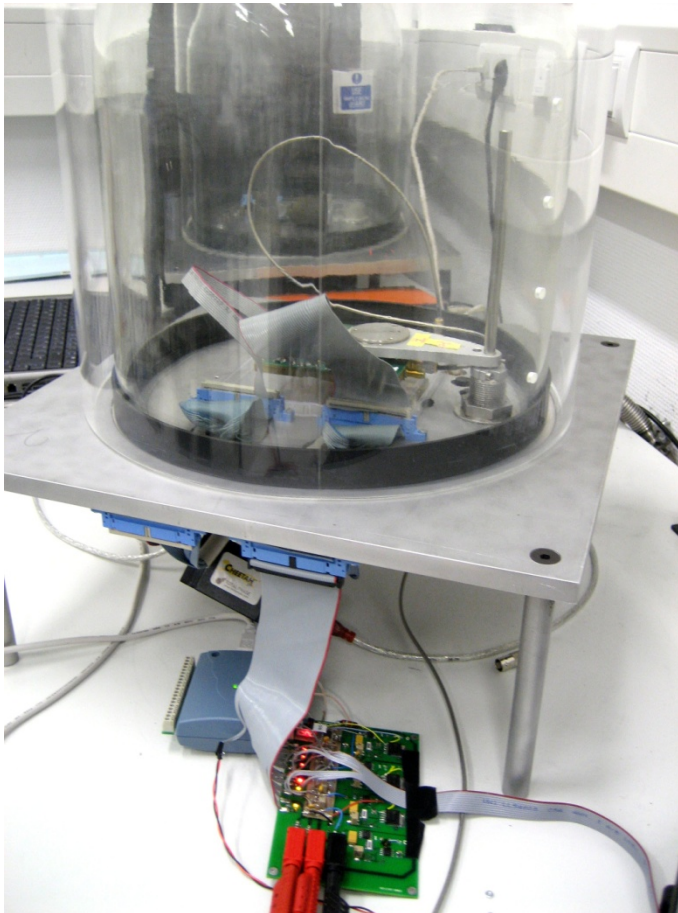


Figure 7. Test setup photo

## 4 Test procedure

### 4.1 Rationale

In order to observe errors, one would like to have a test that creates a predictable result. Especially in the case of SEU:s, since we expect the errors have a very special characteristic (flipped bits).

Since we have no direct access to the digital correlator inputs from the outside but have to go through the samplers, the only way to produce such a highly predictable output is to have a constant DC analog input.

We can bias the input signals to constant high or low. This improves the test, because we get to test for SEU:s both when the different integration counters are running during the measurement and when they are stopped, and also because we get to read out different values than constant zero.

### 4.2 Procedure

Before the test, a dry run of the test hardware and software is performed.

The following procedure was followed:

1. Randomly select one of the 9 possible combinations of I low/high/mid and Q low/high/mid using Labviews random function (uniform distribution).
2. Depending on combination chosen, program the test board input bias voltages to 1.8V for low, 2.15 V for mid, or 2.5 V for high. The high and low reference voltages are kept at 2.0 and 2.3 V, respectively.
3. Integrate for 10 seconds.
4. Read out the data.
5. Check if the overcurrent protection was triggered. If it was, log the event, discard the data and go back to step 2. Otherwise, store the data and go back to step 1.

The turnaround time for step 1-5 is about 10.3 seconds.

In parallel, the data logger logs the voltages, currents and temperature with 60 second interval.

### 4.3 Data processing

The data is checked by comparing the time counter value, multiplied by +1, 0, or -1 depending on input signal chosen, to each of the correlation values.

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## 5 Results

### 5.1 Dry run

The dry run lasted for five hours and the test ran successfully.

It was observed from the dry run that deviations of up to 6 correlator counts can occur naturally in the data\*. The reason for this is that, unlike the slow counters, the fast counters are not reset between the measurements so the contents of the fast counters at startup can vary between different correlation lags.

*\*) For each measurement value, there are four up-counting and four down-counting prescalers. Depending on starting stage, these can start with all up-counting prescalers at zero, and all down-counting at close to overflow, or vice-versa. So in theory it could be possible to get deviations of  $\pm 4$  between lags but this seems to be very unlikely in practice.*

*Note that for noise input, you get a natural randomization of the starting value for each prescaler which makes the impact on the data much smaller than in this test.*

### 5.2 Test notes

The series lasted 18.5 hours, from 7/7 2009 14:24 to 8/7 09:05.

No overcurrent conditions occurred during the measurement.

There was also an effort to measure a second set with 2.1 V CMOS voltage, however due to a mistake during setup, the wrong voltage rail was changed. This data series was therefore discarded.

### 5.3 Bit Error Summary

Total elapsed time: 67496s, Total correlation time from data: 65863s  $\rightarrow$  97.6% time in integration

Total number of measurements: 6552

Measurements containing 1 bit error: 880, 2 bit errors: 79, 3 bit errors: 6, >3 bit errors: 0

Total number of detected bit errors: 1056

No measurements with more than one error in the same correlation bin were taken.

### 5.4 Bit errors versus test case

The number of bit errors occurring in different test cases are tabulated below:

**Table 1. Bit errors by test case for 1.8V series**

Case	I = -1	I = 0	I = +1
Q = +1	135	118	142
Q = 0	86	90	78
Q = -1	145	124	138

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We can see in Table 1 that most bit errors are produced in the cases where both inputs are non-zero (colored blue). There can be several explanations for this phenomena, but the most likely can be an asymmetry in the ripple counter implementation with a master slave flip-flop in the ripple counters. A schematic level analyses indicates that it is therefore likely that due to the assymetry in the design, the slave latch is more SEU sensitive than the master latch, and that's why we see a higher error rate for non-zero counter values. The different sensitivity to SEU has no practical impact as random noise will be processed and a bit error will still be a bit error.

## 5.5 Bit errors over time

Taking the number of bit errors in each measurement and plotting as a time series results in the plots below. From the figure, the errors appear to be randomly distributed over time.

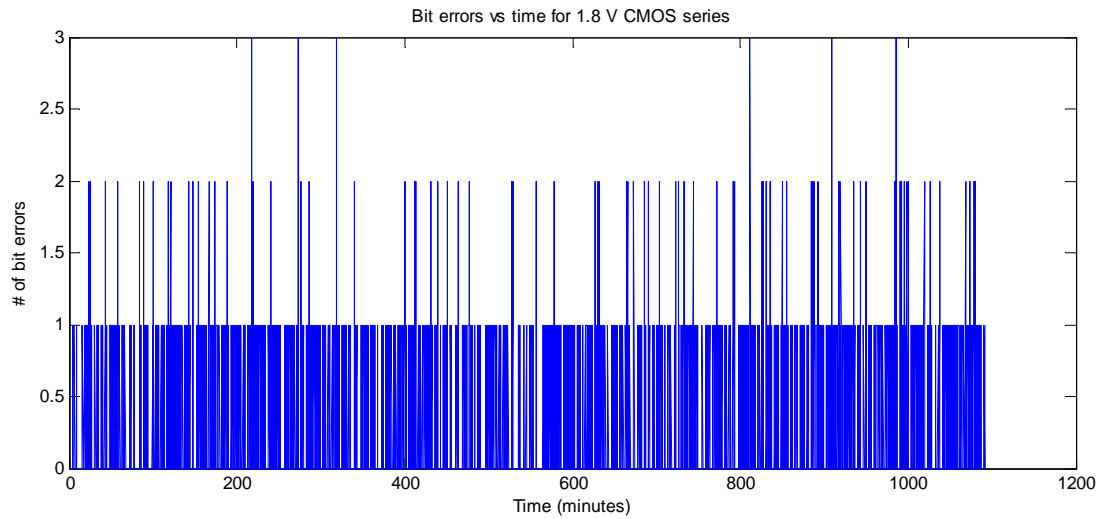


Figure 8. Plots of bit errors vs time for

## 5.6 Bit Error Locations

The locations of the bit errors are plotted below.

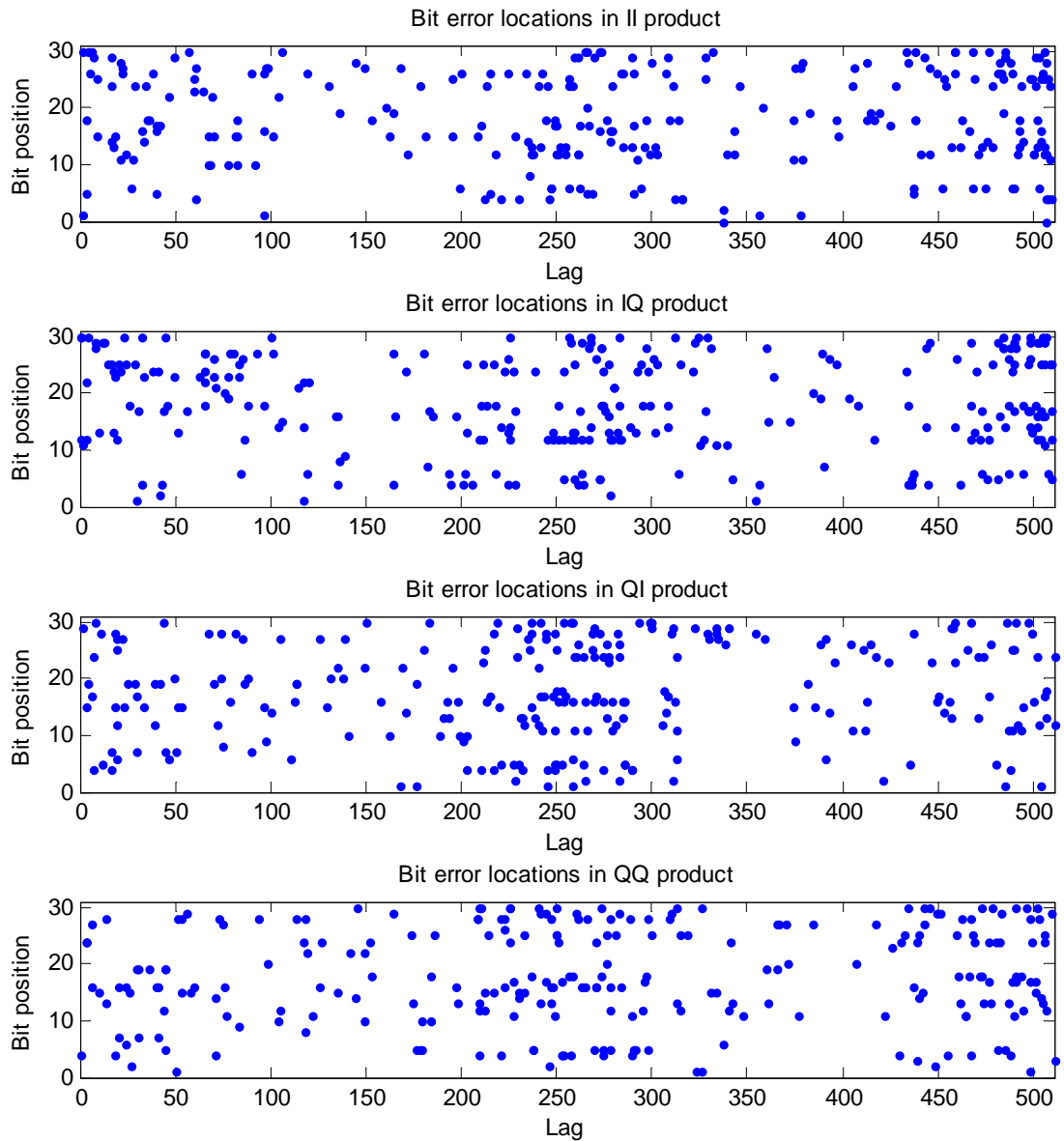


Figure 9. Plot of bit error locations in the correlator

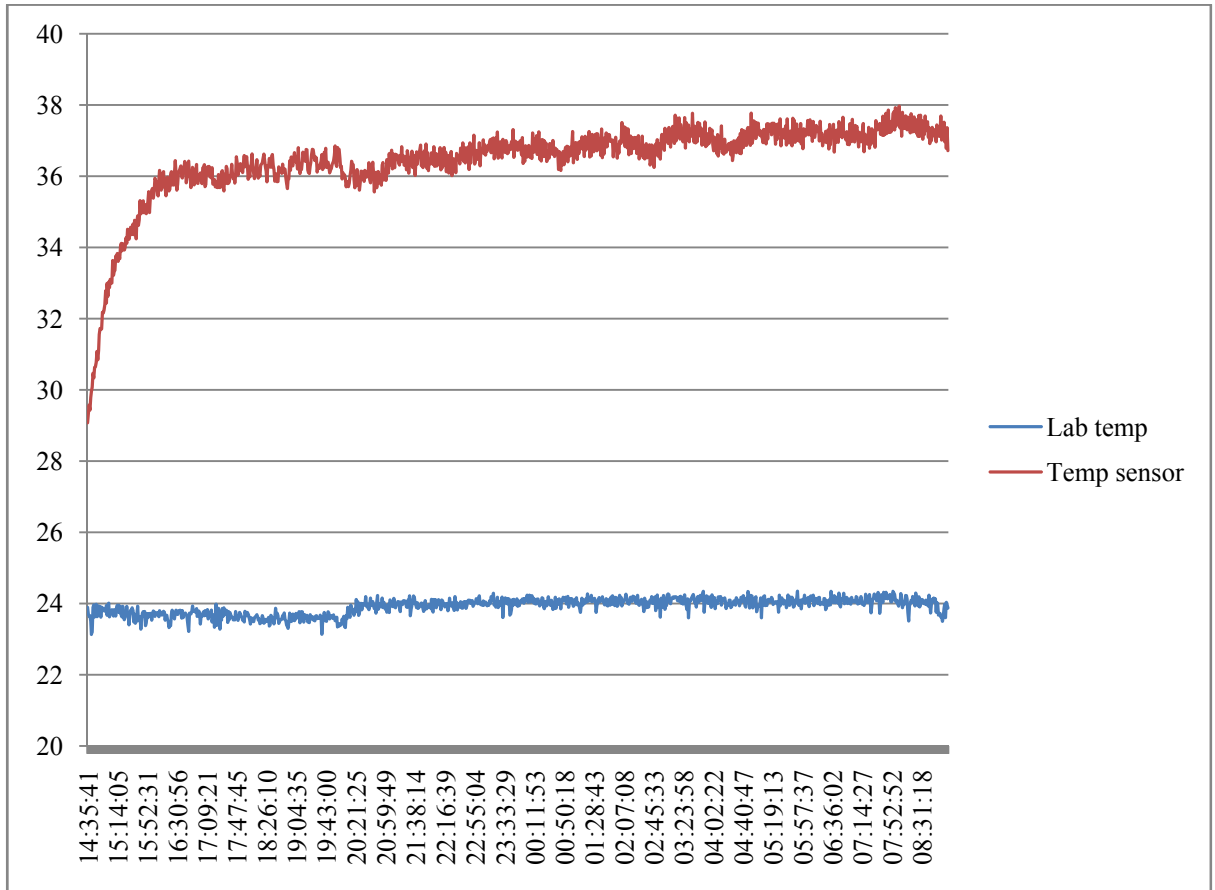
The errors appear to be randomly distributed across the different products and bit positions.

There seems to be a slightly higher error density around lags 0-50, 200-300, and 450-500. These lags correspond to a specific area on the chip, so this could have to do with the positioning of the radiation source.



## 5.7 Logger data

### 5.7.1 Temperature



**Figure 10. Temperature log**

The lab temperature is approximately 23.5-24 degrees during the entire test.

On the temp sensor we can see first a faster transition from 29 to 36 degrees, followed by a slower trend towards 38 degrees.

## 5.7.2 Voltage

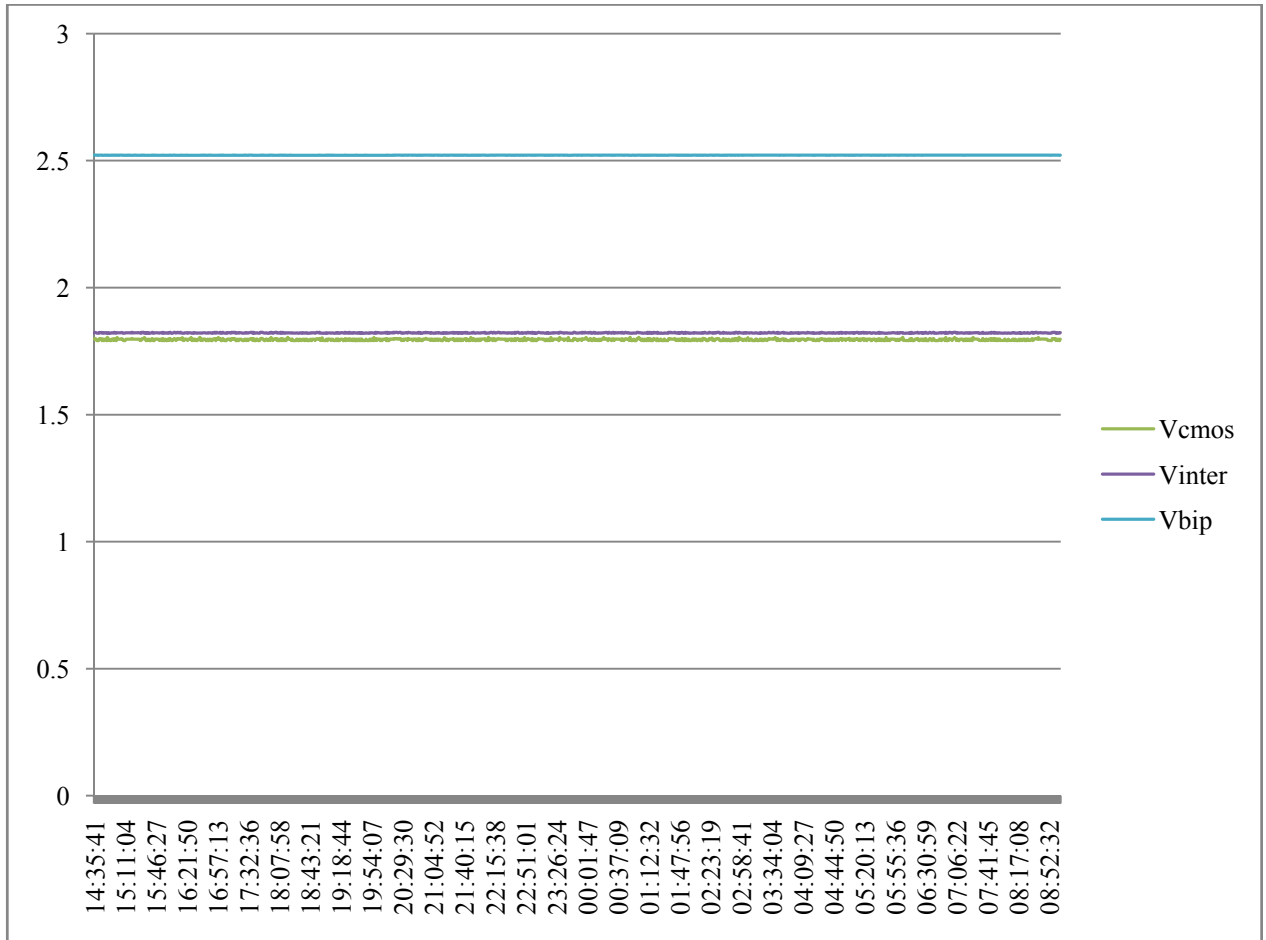


Figure 11. Voltage log

The voltages remain stable during the test.

The average voltages are 1.80V for the CMOS voltage, 1.82V for the Bipolar-to-CMOS interface and 2.52V for the Bipolar supply voltage. The reason for being slightly off the nominal voltages is simply because they have been set by hand using a trim potentiometer.

The 3.3V IO supply voltage was not logged due to limitations on the test setup. However we have no reason to believe that is any less stable than the voltages above.

### 5.7.3 Current

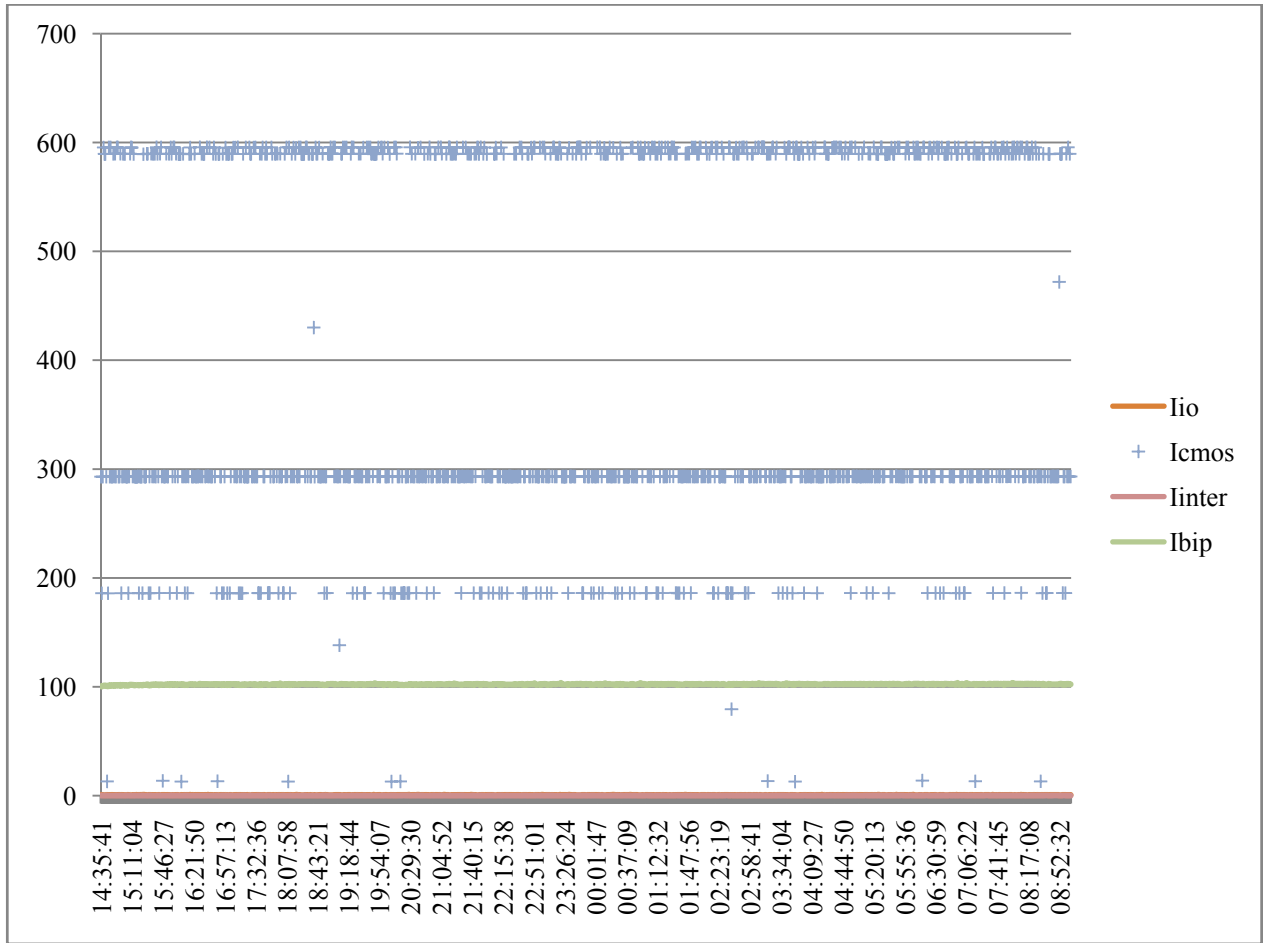


Figure 12. Supply current log

The I/O supply rail current (Iio) and Bipolar-to-CMOS interface supply current (Iinter) are very low (below 1 mA) throughout the test.

The bipolar supply (Ibip) consumes fairly constant current through the test. The average current is 102 mA, giving a power consumption of 257 mW. This is consistent with earlier test measurements.

The CMOS current varies wildly, and to make the log readable this data has been plotted without lines. The reason for this variation is that the CMOS current varies with the different test modes that are randomly selected during the test.

We have three major “bands” of CMOS currents around 186 mA, 293 mA, and 590 mA, plus a handful of points at low currents (<20 mA) and a few points in between.

The CMOS read-outs around 186 mA correspond to the case  $I=0, Q=0$  where no counters are toggling in the accumulators.

The read-outs around 293 mA correspond to the four cases where one of the products is zero where only 25% of the counters are toggling, and the read-outs around 590 mA correspond to the four cases where both signals are non-zero so all counters are running. Note that  $590-186 \text{ mA} =$

404 mA and  $293 - 186 \text{ mA} = 107 \text{ mA}$ , so the additional current is proportional to the number of counters running.

The CMOS read-outs at low currents happens because the data logger sampled the CMOS current during read-out or when the chip was idle, and the in-between points is because the data logger sampled the current at a transition between the modes.

## 6 Analysis

### 6.1 SEU Statistics

The number of SEU:s occurring during an interval of time should follow a Poisson distribution if the SEU:s are independent events.

This means that the probability  $P_N$  of seeing exactly N events during the time interval should be:

$$P_0 = e^{-\lambda s}$$

$$P_N = P_{N-1} \lambda s / N$$

Where  $\lambda$  is the rate of events and s is the interval size.

In our case we have interval  $s = t_{\text{meas}}$  and rate  $\lambda = n_{\text{SEU}} / (n_{\text{meas}} t_{\text{meas}})$ , where

$t_{\text{meas}}$  is the average measurement time,

$n_{\text{meas}}$  is the number of measurements

$n_{\text{SEU}}$  is the total number of SEU:s detected

thus,  $\lambda s = n_{\text{SEU}} / n_{\text{meas}}$  and the formula becomes:

$$P_0 = e^{-n_{\text{SEU}}/n_{\text{meas}}}$$

$$P_1 = P_0 n_{\text{SEU}}/n_{\text{meas}}$$

$$P_2 = P_1 n_{\text{SEU}}/n_{\text{meas}} / 2$$

$$P_3 = P_2 n_{\text{SEU}}/n_{\text{meas}} / 3$$

Applying this for the series and comparing with the measured values gives the results below:

Value	Expected	Measured
$n_{\text{meas}}$		6552
$n_{\text{SEU}}$		1056
$P_0 n_{\text{meas}}$	5576	5587
$P_1 n_{\text{meas}}$	899	880
$P_2 n_{\text{meas}}$	72	79
$P_3 n_{\text{meas}}$	4	6

We can see that the measured distribution follows the Poisson distribution very well, supporting the assumption that the bit errors are independent events occurring at a constant rate.

## 6.2 Cross-section Calculations

### 6.2.1 SEU Cross section

A total of 1056 SEU:s occurred over an effective integration time period of 65863 seconds. This gives an error rate of  $1056/65863 = 0.016$  errors/s = 0.962 errors/min.

The distance between the source and the chip was estimated to 7 mm. This gives an ion flux of approximately 3000 ions / cm<sup>2</sup> min.

The measured SEU cross section thus becomes  $0.962/3000 = 0.00032$  errors cm<sup>2</sup> / ions.

The errors of  $\pm 6$  counts, as mentioned in section 5.1, were compensated out, by letting each value to be changed  $\pm 3$  counts to create as few bit errors as possible. In order to still get a fair cross-section value, the lowest 3 bits were removed from the nominator in the cross-section so the number of flip-flops were taken as  $2048*2*28$ .

### 6.2.2 SEU Cross section per flip-flop

The read-out data are 2048 values, each value is 32 bits long. In the chip, each read-out value is generated from two 31-bit long registers with positive and negative counts, that are subtracted on the fly by the read-out logic into one 32-bit signed value. Thus, the total number of counter bits affecting the read-out data is  $2048 \times 2 \times 31 = 126976$  bits. Each counter bit is made of a T flip-flop.

Because of the natural variations in the data explained in Section 5.1, we can not detect all errors in the last 3 bits. Therefore we estimate the number of flip flops covered by the test as  $2048 \times 2 \times (31-3) = 114688$  flip-flops.

The measured SEU cross-section per flip-flop therefore becomes:  
 $0.00032/114688 = 2.8 \times 10^{-9}$  errors cm<sup>2</sup> / ff ions

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## 7 Conclusions

The testing has shown that under Californium radiation, single-event upsets are produced in the HIFAS chip. These upsets cause bits to toggle in the read-out data.

All events appear to be independent (based on statistical analysis) and we see no significant trends over time. No multiple-bit upsets have been detected.

No latch-ups were detected during the tests.

The SEU cross-section for the chip was calculated to  $0.00032 \text{ cm}^2$  and the cross-section per flip-flop was calculated to  $2.8 \times 10^{-9} \text{ cm}^2$ .

The accuracy in these cross-section figures is much less than the two digits shown above, due to uncertainties of the source and test setup. In addition, the translation from Californium to heavy ions in space is difficult because of the difference in the radiation's penetration depth and the many layers of metallisation and passivation on the chip. An error of more than one order of magnitude should be admitted.

Several approaches for mitigating the bit errors in an application have been presented. Most of these approaches are purely made in post-processing without any additional hardware.

---

## 8 Appendix: List of detected SEU:s

```
[#22 I=1 Q=-1] 07-jul-2009 14:27:44 (+0:3:34):  
Bit error in QQ lag 407 bit 20  
[#24 I=1 Q=0] 07-jul-2009 14:28:06 (+0:3:56):  
Bit error in IQ lag 104 bit 14  
[#39 I=-1 Q=0] 07-jul-2009 14:30:40 (+0:6:30):  
Bit error in QI lag 242 bit 11  
[#56 I=-1 Q=0] 07-jul-2009 14:33:34 (+0:9:24):  
Bit error in QI lag 283 bit 24  
[#57 I=1 Q=1] 07-jul-2009 14:33:44 (+0:9:34):  
Bit error in QQ lag 271 bit 16  
[#97 I=-1 Q=0] 07-jul-2009 14:40:34 (+0:16:24):  
Bit error in II lag 269 bit 5  
[#108 I=0 Q=1] 07-jul-2009 14:42:28 (+0:18:18):  
Bit error in QI lag 271 bit 17  
[#110 I=1 Q=1] 07-jul-2009 14:42:48 (+0:18:38):  
Bit error in IQ lag 224 bit 13  
[#116 I=0 Q=-1] 07-jul-2009 14:43:50 (+0:19:40):  
Bit error in QQ lag 505 bit 13  
[#119 I=1 Q=0] 07-jul-2009 14:44:20 (+0:20:10):  
Bit error in QQ lag 221 bit 4  
[#133 I=0 Q=-1] 07-jul-2009 14:46:44 (+0:22:34):  
Bit error in IQ lag 510 bit 17  
[#138 I=0 Q=-1] 07-jul-2009 14:47:36 (+0:23:26):  
Bit error in QQ lag 53 bit 28  
Bit error in QQ lag 434 bit 30  
[#149 I=1 Q=1] 07-jul-2009 14:49:30 (+0:25:20):  
Bit error in II lag 237 bit 13  
Bit error in II lag 459 bit 30  
[#155 I=0 Q=1] 07-jul-2009 14:50:32 (+0:26:22):  
Bit error in II lag 501 bit 29  
[#160 I=-1 Q=-1] 07-jul-2009 14:51:24 (+0:27:14):  
Bit error in II lag 27 bit 6  
[#178 I=1 Q=1] 07-jul-2009 14:54:28 (+0:30:18):  
Bit error in II lag 490 bit 6  
[#184 I=1 Q=1] 07-jul-2009 14:55:30 (+0:31:20):  
Bit error in QI lag 274 bit 24  
[#185 I=1 Q=1] 07-jul-2009 14:55:40 (+0:31:30):  
Bit error in II lag 504 bit 25  
[#197 I=-1 Q=1] 07-jul-2009 14:57:42 (+0:33:32):  
Bit error in II lag 375 bit 27  
[#199 I=-1 Q=-1] 07-jul-2009 14:58:04 (+0:33:54):  
Bit error in IQ lag 180 bit 27  
[#209 I=1 Q=1] 07-jul-2009 14:59:46 (+0:35:36):  
Bit error in QI lag 139 bit 27  
[#213 I=1 Q=1] 07-jul-2009 15:00:28 (+0:36:18):  
Bit error in QQ lag 334 bit 15  
[#228 I=1 Q=0] 07-jul-2009 15:03:02 (+0:38:52):  
Bit error in QI lag 176 bit 19  
[#229 I=0 Q=-1] 07-jul-2009 15:03:12 (+0:39:2):  
Bit error in II lag 493 bit 13  
[#236 I=-1 Q=-1] 07-jul-2009 15:04:24 (+0:40:14):  
Bit error in II lag 212 bit 4  
[#255 I=0 Q=-1] 07-jul-2009 15:07:38 (+0:43:28):  
Bit error in IQ lag 203 bit 13  
[#257 I=1 Q=-1] 07-jul-2009 15:08:00 (+0:43:50):  
Bit error in IQ lag 264 bit 4  
[#258 I=-1 Q=-1] 07-jul-2009 15:08:10 (+0:43:60):  
Bit error in II lag 96 bit 26  
[#263 I=1 Q=-1] 07-jul-2009 15:09:00 (+0:44:50):  
Bit error in II lag 337 bit 2  
Bit error in QI lag 486 bit 30  
[#267 I=-1 Q=1] 07-jul-2009 15:09:42 (+0:45:32):  
Bit error in QI lag 474 bit 24  
[#289 I=1 Q=-1] 07-jul-2009 15:13:28 (+0:49:18):  
Bit error in II lag 38 bit 26  
[#293 I=-1 Q=1] 07-jul-2009 15:14:10 (+0:49:60):  
Bit error in QQ lag 283 bit 29  
[#294 I=-1 Q=0] 07-jul-2009 15:14:20 (+0:50:10):  
Bit error in II lag 437 bit 5  
[#303 I=1 Q=0] 07-jul-2009 15:15:52 (+0:51:42):  
Bit error in II lag 3 bit 5  
[#306 I=1 Q=0] 07-jul-2009 15:16:22 (+0:52:12):  
Bit error in IQ lag 490 bit 17  
[#314 I=-1 Q=0] 07-jul-2009 15:17:44 (+0:53:34):  
Bit error in IQ lag 484 bit 28  
[#329 I=-1 Q=1] 07-jul-2009 15:20:18 (+0:56:8):  
Bit error in II lag 172 bit 12  
[#331 I=0 Q=-1] 07-jul-2009 15:20:40 (+0:56:30):
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Bit error in II lag 273 bit 30  
[#340 I=0 Q=-1] 07-jul-2009 15:22:12 (+0:58:2):  
Bit error in II lag 468 bit 30  
[#345 I=0 Q=-1] 07-jul-2009 15:23:04 (+0:58:54):  
Bit error in IQ lag 473 bit 6  
Bit error in QQ lag 223 bit 28  
[#347 I=-1 Q=0] 07-jul-2009 15:23:24 (+0:59:14):  
Bit error in QQ lag 71 bit 4  
[#350 I=0 Q=-1] 07-jul-2009 15:23:54 (+0:59:44):  
Bit error in QI lag 210 bit 4  
[#351 I=1 Q=1] 07-jul-2009 15:24:06 (+0:59:56):  
Bit error in IQ lag 308 bit 18  
[#355 I=-1 Q=1] 07-jul-2009 15:24:46 (+1:0:36):  
Bit error in IQ lag 65 bit 27  
[#359 I=0 Q=-1] 07-jul-2009 15:25:28 (+1:1:18):  
Bit error in II lag 70 bit 15  
[#361 I=0 Q=1] 07-jul-2009 15:25:48 (+1:1:38):  
Bit error in QI lag 254 bit 30  
[#375 I=0 Q=-1] 07-jul-2009 15:28:12 (+1:4:2):  
Bit error in IQ lag 134 bit 16  
[#381 I=-1 Q=1] 07-jul-2009 15:29:14 (+1:5:4):  
Bit error in QI lag 33 bit 15  
[#382 I=1 Q=0] 07-jul-2009 15:29:24 (+1:5:14):  
Bit error in IQ lag 78 bit 23  
[#391 I=0 Q=-1] 07-jul-2009 15:30:56 (+1:6:46):  
Bit error in QI lag 477 bit 17  
[#393 I=0 Q=-1] 07-jul-2009 15:31:16 (+1:7:6):  
Bit error in II lag 119 bit 26  
[#394 I=1 Q=-1] 07-jul-2009 15:31:28 (+1:7:18):  
Bit error in QI lag 277 bit 23  
[#401 I=0 Q=0] 07-jul-2009 15:32:40 (+1:8:30):  
Bit error in II lag 285 bit 13  
[#435 I=0 Q=-1] 07-jul-2009 15:38:28 (+1:14:18):  
Bit error in QQ lag 488 bit 4  
[#441 I=-1 Q=1] 07-jul-2009 15:39:30 (+1:15:20):  
Bit error in QQ lag 257 bit 4  
[#468 I=0 Q=-1] 07-jul-2009 15:44:06 (+1:19:56):  
Bit error in QI lag 424 bit 23  
[#503 I=-1 Q=0] 07-jul-2009 15:50:06 (+1:25:56):  
Bit error in II lag 17 bit 13  
[#506 I=-1 Q=-1] 07-jul-2009 15:50:38 (+1:26:28):  
Bit error in IQ lag 268 bit 30  
Bit error in QQ lag 198 bit 13  
[#517 I=-1 Q=1] 07-jul-2009 15:52:30 (+1:28:20):  
Bit error in QI lag 307 bit 14  
[#533 I=-1 Q=-1] 07-jul-2009 15:55:14 (+1:31:4):  
Bit error in II lag 316 bit 4  
Bit error in QI lag 458 bit 29  
[#539 I=-1 Q=-1] 07-jul-2009 15:56:16 (+1:32:6):  
Bit error in QQ lag 209 bit 30  
[#547 I=-1 Q=0] 07-jul-2009 15:57:38 (+1:33:28):  
Bit error in QQ lag 477 bit 24  
[#549 I=1 Q=1] 07-jul-2009 15:57:58 (+1:33:48):  
Bit error in QQ lag 503 bit 28  
[#550 I=-1 Q=0] 07-jul-2009 15:58:10 (+1:33:60):  
Bit error in QQ lag 506 bit 25  
[#559 I=1 Q=-1] 07-jul-2009 15:59:42 (+1:35:32):  
Bit error in QQ lag 472 bit 18  
[#597 I=1 Q=1] 07-jul-2009 16:06:12 (+1:42:2):  
Bit error in II lag 446 bit 12  
[#602 I=1 Q=-1] 07-jul-2009 16:07:04 (+1:42:54):  
Bit error in QQ lag 254 bit 4  
[#604 I=0 Q=-1] 07-jul-2009 16:07:24 (+1:43:14):  
Bit error in II lag 428 bit 24  
Bit error in QI lag 480 bit 5  
[#606 I=1 Q=1] 07-jul-2009 16:07:44 (+1:43:34):  
Bit error in QQ lag 241 bit 13  
[#621 I=-1 Q=1] 07-jul-2009 16:10:18 (+1:46:8):  
Bit error in II lag 61 bit 27  
[#624 I=1 Q=-1] 07-jul-2009 16:10:50 (+1:46:40):  
Bit error in QI lag 141 bit 10  
[#628 I=1 Q=-1] 07-jul-2009 16:11:30 (+1:47:20):  
Bit error in II lag 279 bit 16  
[#639 I=1 Q=-1] 07-jul-2009 16:13:24 (+1:49:14):  
Bit error in QQ lag 313 bit 30  
[#656 I=1 Q=1] 07-jul-2009 16:16:18 (+1:52:8):  
Bit error in QI lag 19 bit 6  
[#662 I=-1 Q=0] 07-jul-2009 16:17:20 (+1:53:10):  
Bit error in IQ lag 434 bit 18  
[#666 I=-1 Q=1] 07-jul-2009 16:18:00 (+1:53:50):  
Bit error in QQ lag 56 bit 29  
[#669 I=0 Q=1] 07-jul-2009 16:18:32 (+1:54:22):  
Bit error in II lag 60 bit 25  
[#671 I=-1 Q=0] 07-jul-2009 16:18:52 (+1:54:42):

Bit error in IQ lag 254 bit 24  
[#674 I=1 Q=1] 07-jul-2009 16:19:24 (+1:55:14):  
Bit error in IQ lag 505 bit 16  
[#680 I=0 Q=1] 07-jul-2009 16:20:24 (+1:56:14):  
Bit error in IQ lag 65 bit 24  
[#698 I=1 Q=-1] 07-jul-2009 16:23:30 (+1:59:20):  
Bit error in II lag 500 bit 12  
[#699 I=0 Q=-1] 07-jul-2009 16:23:40 (+1:59:30):  
Bit error in II lag 251 bit 12  
[#701 I=-1 Q=0] 07-jul-2009 16:24:00 (+1:59:50):  
Bit error in II lag 506 bit 30  
[#713 I=1 Q=-1] 07-jul-2009 16:26:04 (+2:1:54):  
Bit error in II lag 242 bit 26  
Bit error in QI lag 113 bit 19  
[#715 I=-1 Q=1] 07-jul-2009 16:26:24 (+2:2:14):  
Bit error in QQ lag 45 bit 19  
[#716 I=-1 Q=0] 07-jul-2009 16:26:34 (+2:2:24):  
Bit error in QQ lag 377 bit 11  
[#724 I=1 Q=-1] 07-jul-2009 16:27:56 (+2:3:46):  
Bit error in QI lag 285 bit 13  
Bit error in QQ lag 511 bit 3  
[#725 I=-1 Q=1] 07-jul-2009 16:28:08 (+2:3:58):  
Bit error in QI lag 11 bit 28  
[#730 I=-1 Q=1] 07-jul-2009 16:28:58 (+2:4:48):  
Bit error in IQ lag 277 bit 25  
[#733 I=-1 Q=1] 07-jul-2009 16:29:30 (+2:5:20):  
Bit error in II lag 488 bit 28  
[#737 I=1 Q=0] 07-jul-2009 16:30:10 (+2:5:60):  
Bit error in QI lag 311 bit 29  
[#740 I=-1 Q=1] 07-jul-2009 16:30:42 (+2:6:32):  
Bit error in QQ lag 149 bit 10  
[#745 I=1 Q=-1] 07-jul-2009 16:31:32 (+2:7:22):  
Bit error in IQ lag 280 bit 21  
[#751 I=-1 Q=1] 07-jul-2009 16:32:34 (+2:8:24):  
Bit error in QI lag 279 bit 11  
[#757 I=1 Q=-1] 07-jul-2009 16:33:36 (+2:9:26):  
Bit error in II lag 33 bit 14  
[#759 I=1 Q=-1] 07-jul-2009 16:33:56 (+2:9:46):  
Bit error in QI lag 305 bit 12  
[#761 I=-1 Q=1] 07-jul-2009 16:34:18 (+2:10:8):  
Bit error in II lag 101 bit 15  
[#765 I=1 Q=1] 07-jul-2009 16:34:58 (+2:10:48):  
Bit error in IQ lag 481 bit 29  
[#767 I=0 Q=1] 07-jul-2009 16:35:20 (+2:11:10):  
Bit error in QI lag 30 bit 7  
[#779 I=1 Q=1] 07-jul-2009 16:37:22 (+2:13:12):  
Bit error in IQ lag 197 bit 16  
[#781 I=1 Q=0] 07-jul-2009 16:37:42 (+2:13:32):  
Bit error in QQ lag 208 bit 28  
[#789 I=-1 Q=1] 07-jul-2009 16:39:06 (+2:14:56):  
Bit error in QQ lag 258 bit 18  
[#796 I=-1 Q=-1] 07-jul-2009 16:40:18 (+2:16:8):  
Bit error in II lag 499 bit 25  
[#806 I=1 Q=1] 07-jul-2009 16:42:00 (+2:17:50):  
Bit error in QI lag 16 bit 4  
[#811 I=1 Q=-1] 07-jul-2009 16:42:52 (+2:18:42):  
Bit error in QI lag 25 bit 19  
[#824 I=-1 Q=-1] 07-jul-2009 16:45:04 (+2:20:54):  
Bit error in II lag 266 bit 30  
[#833 I=1 Q=0] 07-jul-2009 16:46:38 (+2:22:28):  
Bit error in QQ lag 10 bit 15  
[#835 I=-1 Q=0] 07-jul-2009 16:46:58 (+2:22:48):  
Bit error in QQ lag 31 bit 7  
[#846 I=1 Q=-1] 07-jul-2009 16:48:50 (+2:24:40):  
Bit error in QQ lag 361 bit 13  
[#853 I=1 Q=0] 07-jul-2009 16:50:02 (+2:25:52):  
Bit error in QI lag 45 bit 7  
Bit error in QI lag 310 bit 28  
[#856 I=-1 Q=1] 07-jul-2009 16:50:34 (+2:26:24):  
Bit error in QI lag 312 bit 16  
Bit error in QI lag 334 bit 29  
[#858 I=0 Q=-1] 07-jul-2009 16:50:54 (+2:26:44):  
Bit error in QQ lag 127 bit 24  
[#861 I=-1 Q=1] 07-jul-2009 16:51:24 (+2:27:14):  
Bit error in II lag 82 bit 18  
[#868 I=-1 Q=1] 07-jul-2009 16:52:36 (+2:28:26):  
Bit error in II lag 130 bit 24  
[#869 I=-1 Q=0] 07-jul-2009 16:52:48 (+2:28:38):  
Bit error in II lag 6 bit 30  
[#884 I=0 Q=-1] 07-jul-2009 16:55:22 (+2:31:12):  
Bit error in QI lag 249 bit 11  
Bit error in QQ lag 122 bit 11  
[#911 I=1 Q=1] 07-jul-2009 16:59:58 (+2:35:48):  
Bit error in QQ lag 41 bit 16

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[#925 I=0 Q=1] 07-jul-2009 17:02:22 (+2:38:12):  
Bit error in II lag 272 bit 30  
[#926 I=1 Q=-1] 07-jul-2009 17:02:32 (+2:38:22):  
Bit error in IQ lag 19 bit 12  
Bit error in QI lag 74 bit 20  
[#928 I=-1 Q=1] 07-jul-2009 17:02:54 (+2:38:44):  
Bit error in IQ lag 42 bit 2  
[#930 I=0 Q=0] 07-jul-2009 17:03:14 (+2:39:4):  
Bit error in II lag 22 bit 27  
[#932 I=0 Q=0] 07-jul-2009 17:03:34 (+2:39:24):  
Bit error in QQ lag 464 bit 11  
[#933 I=1 Q=0] 07-jul-2009 17:03:44 (+2:39:34):  
Bit error in QI lag 259 bit 11  
[#941 I=-1 Q=1] 07-jul-2009 17:05:08 (+2:40:58):  
Bit error in II lag 199 bit 6  
[#944 I=1 Q=-1] 07-jul-2009 17:05:38 (+2:41:28):  
Bit error in II lag 4 bit 30  
[#945 I=-1 Q=1] 07-jul-2009 17:05:48 (+2:41:38):  
Bit error in IQ lag 218 bit 18  
[#954 I=0 Q=1] 07-jul-2009 17:07:20 (+2:43:10):  
Bit error in II lag 302 bit 13  
[#963 I=-1 Q=-1] 07-jul-2009 17:08:54 (+2:44:44):  
Bit error in II lag 160 bit 20  
[#966 I=-1 Q=1] 07-jul-2009 17:09:24 (+2:45:14):  
Bit error in II lag 60 bit 23  
[#974 I=-1 Q=0] 07-jul-2009 17:10:46 (+2:46:36):  
Bit error in IQ lag 342 bit 5  
[#986 I=0 Q=0] 07-jul-2009 17:12:50 (+2:48:40):  
Bit error in QI lag 245 bit 1  
[#998 I=1 Q=1] 07-jul-2009 17:14:52 (+2:50:42):  
Bit error in IQ lag 65 bit 18  
Bit error in QQ lag 467 bit 4  
[#999 I=1 Q=0] 07-jul-2009 17:15:04 (+2:50:54):  
Bit error in QQ lag 290 bit 5  
[#1006 I=0 Q=0] 07-jul-2009 17:16:14 (+2:52:4):  
Bit error in IQ lag 15 bit 25  
[#1013 I=0 Q=0] 07-jul-2009 17:17:26 (+2:53:16):  
Bit error in QI lag 231 bit 13  
[#1022 I=0 Q=0] 07-jul-2009 17:19:00 (+2:54:50):  
Bit error in QI lag 258 bit 1  
[#1023 I=-1 Q=-1] 07-jul-2009 17:19:10 (+2:54:60):  
Bit error in IQ lag 46 bit 18  
[#1029 I=-1 Q=1] 07-jul-2009 17:20:12 (+2:56:2):  
Bit error in IQ lag 259 bit 5  
[#1030 I=1 Q=0] 07-jul-2009 17:20:22 (+2:56:12):  
Bit error in QQ lag 482 bit 24  
[#1038 I=-1 Q=-1] 07-jul-2009 17:21:44 (+2:57:34):  
Bit error in QQ lag 233 bit 25  
[#1039 I=0 Q=0] 07-jul-2009 17:21:54 (+2:57:44):  
Bit error in QQ lag 485 bit 5  
[#1042 I=0 Q=1] 07-jul-2009 17:22:26 (+2:58:16):  
Bit error in IQ lag 322 bit 29  
Bit error in QI lag 138 bit 20  
[#1052 I=0 Q=1] 07-jul-2009 17:24:08 (+2:59:58):  
Bit error in II lag 278 bit 14  
[#1056 I=0 Q=-1] 07-jul-2009 17:24:48 (+3:0:38):  
Bit error in IQ lag 301 bit 26  
[#1057 I=1 Q=1] 07-jul-2009 17:25:00 (+3:0:50):  
Bit error in IQ lag 262 bit 17  
[#1063 I=1 Q=0] 07-jul-2009 17:26:00 (+3:1:50):  
Bit error in QI lag 487 bit 11  
[#1065 I=-1 Q=1] 07-jul-2009 17:26:22 (+3:2:12):  
Bit error in QQ lag 253 bit 4  
[#1076 I=1 Q=-1] 07-jul-2009 17:28:14 (+3:4:4):  
Bit error in QI lag 393 bit 14  
[#1090 I=1 Q=0] 07-jul-2009 17:30:38 (+3:6:28):  
Bit error in QQ lag 227 bit 11  
[#1103 I=-1 Q=-1] 07-jul-2009 17:32:52 (+3:8:42):  
Bit error in QI lag 300 bit 29  
[#1106 I=0 Q=-1] 07-jul-2009 17:33:22 (+3:9:12):  
Bit error in QI lag 338 bit 26  
[#1116 I=0 Q=1] 07-jul-2009 17:35:06 (+3:10:56):  
Bit error in II lag 483 bit 26  
[#1120 I=-1 Q=1] 07-jul-2009 17:35:46 (+3:11:36):  
Bit error in QQ lag 271 bit 24  
[#1123 I=-1 Q=0] 07-jul-2009 17:36:18 (+3:12:8):  
Bit error in QI lag 282 bit 28  
[#1130 I=1 Q=-1] 07-jul-2009 17:37:30 (+3:13:20):  
Bit error in QI lag 243 bit 17  
[#1131 I=0 Q=0] 07-jul-2009 17:37:40 (+3:13:30):  
Bit error in II lag 434 bit 28  
Bit error in QI lag 212 bit 25  
[#1138 I=0 Q=-1] 07-jul-2009 17:38:52 (+3:14:42):  
Bit error in IQ lag 459 bit 14
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[#1156 I=0 Q=1] 07-jul-2009 17:41:56 (+3:17:46):  
Bit error in II lag 356 bit 1  
[#1165 I=1 Q=1] 07-jul-2009 17:43:30 (+3:19:20):  
Bit error in IQ lag 182 bit 7  
[#1172 I=0 Q=1] 07-jul-2009 17:44:42 (+3:20:32):  
Bit error in IQ lag 263 bit 29  
[#1176 I=0 Q=-1] 07-jul-2009 17:45:22 (+3:21:12):  
Bit error in IQ lag 3 bit 22  
[#1178 I=-1 Q=1] 07-jul-2009 17:45:44 (+3:21:34):  
Bit error in IQ lag 12 bit 29  
[#1182 I=0 Q=-1] 07-jul-2009 17:46:24 (+3:22:14):  
Bit error in IQ lag 510 bit 25  
[#1188 I=0 Q=-1] 07-jul-2009 17:47:26 (+3:23:16):  
Bit error in IQ lag 491 bit 30  
[#1193 I=0 Q=1] 07-jul-2009 17:48:18 (+3:24:8):  
Bit error in QI lag 511 bit 24  
[#1200 I=-1 Q=-1] 07-jul-2009 17:49:30 (+3:25:20):  
Bit error in QI lag 253 bit 18  
[#1202 I=0 Q=0] 07-jul-2009 17:49:50 (+3:25:40):  
Bit error in QI lag 79 bit 16  
[#1206 I=1 Q=-1] 07-jul-2009 17:50:32 (+3:26:22):  
Bit error in QQ lag 223 bit 26  
[#1211 I=0 Q=0] 07-jul-2009 17:51:22 (+3:27:12):  
Bit error in QQ lag 430 bit 4  
[#1216 I=-1 Q=1] 07-jul-2009 17:52:14 (+3:28:4):  
Bit error in IQ lag 201 bit 4  
[#1217 I=-1 Q=1] 07-jul-2009 17:52:24 (+3:28:14):  
Bit error in II lag 230 bit 4  
[#1222 I=0 Q=0] 07-jul-2009 17:53:16 (+3:29:6):  
Bit error in IQ lag 445 bit 4  
[#1230 I=1 Q=-1] 07-jul-2009 17:54:38 (+3:30:28):  
Bit error in QQ lag 230 bit 15  
[#1243 I=-1 Q=-1] 07-jul-2009 17:56:52 (+3:32:42):  
Bit error in QQ lag 118 bit 8  
[#1247 I=0 Q=0] 07-jul-2009 17:57:32 (+3:33:22):  
Bit error in IQ lag 100 bit 30  
[#1260 I=0 Q=0] 07-jul-2009 17:59:46 (+3:35:36):  
Bit error in QQ lag 426 bit 23  
[#1279 I=-1 Q=-1] 07-jul-2009 18:03:02 (+3:38:52):  
Bit error in QQ lag 276 bit 25  
[#1286 I=0 Q=0] 07-jul-2009 18:04:14 (+3:40:4):  
Bit error in QI lag 499 bit 28  
[#1291 I=1 Q=1] 07-jul-2009 18:05:06 (+3:40:56):  
Bit error in II lag 303 bit 12  
[#1294 I=1 Q=1] 07-jul-2009 18:05:36 (+3:41:26):  
Bit error in QI lag 199 bit 10  
[#1305 I=-1 Q=-1] 07-jul-2009 18:07:30 (+3:43:20):  
Bit error in II lag 509 bit 11  
Bit error in IQ lag 491 bit 28  
Bit error in QI lag 334 bit 28  
[#1307 I=-1 Q=1] 07-jul-2009 18:07:50 (+3:43:40):  
Bit error in II lag 358 bit 20  
[#1318 I=-1 Q=-1] 07-jul-2009 18:09:42 (+3:45:32):  
Bit error in QQ lag 468 bit 25  
[#1319 I=1 Q=1] 07-jul-2009 18:09:54 (+3:45:44):  
Bit error in QQ lag 20 bit 16  
Bit error in QQ lag 221 bit 28  
[#1321 I=0 Q=0] 07-jul-2009 18:10:14 (+3:46:4):  
Bit error in QQ lag 480 bit 24  
[#1323 I=-1 Q=-1] 07-jul-2009 18:10:34 (+3:46:24):  
Bit error in IQ lag 106 bit 15  
[#1325 I=0 Q=-1] 07-jul-2009 18:10:56 (+3:46:46):  
Bit error in QI lag 158 bit 16  
[#1326 I=-1 Q=0] 07-jul-2009 18:11:06 (+3:46:56):  
Bit error in QQ lag 501 bit 15  
[#1333 I=0 Q=0] 07-jul-2009 18:12:18 (+3:48:8):  
Bit error in QI lag 258 bit 16  
[#1334 I=0 Q=-1] 07-jul-2009 18:12:28 (+3:48:18):  
Bit error in QI lag 456 bit 16  
[#1342 I=0 Q=-1] 07-jul-2009 18:13:50 (+3:49:40):  
Bit error in II lag 378 bit 27  
[#1346 I=1 Q=1] 07-jul-2009 18:14:32 (+3:50:22):  
Bit error in QI lag 283 bit 26  
[#1354 I=-1 Q=0] 07-jul-2009 18:15:54 (+3:51:44):  
Bit error in IQ lag 256 bit 30  
[#1355 I=-1 Q=1] 07-jul-2009 18:16:04 (+3:51:54):  
Bit error in IQ lag 510 bit 12  
[#1361 I=1 Q=0] 07-jul-2009 18:17:06 (+3:52:56):  
Bit error in QQ lag 251 bit 24  
[#1366 I=1 Q=1] 07-jul-2009 18:17:58 (+3:53:48):  
Bit error in II lag 256 bit 25  
[#1369 I=1 Q=-1] 07-jul-2009 18:18:28 (+3:54:18):  
Bit error in II lag 285 bit 26  
[#1371 I=0 Q=1] 07-jul-2009 18:18:48 (+3:54:38):

Bit error in IQ lag 303 bit 25  
[#1375 I=-1 Q=-1] 07-jul-2009 18:19:30 (+3:55:20):  
Bit error in IQ lag 4 bit 30  
[#1378 I=-1 Q=-1] 07-jul-2009 18:20:02 (+3:55:52):  
Bit error in IQ lag 506 bit 29  
[#1388 I=1 Q=1] 07-jul-2009 18:21:44 (+3:57:34):  
Bit error in QQ lag 439 bit 24  
[#1393 I=-1 Q=1] 07-jul-2009 18:22:36 (+3:58:26):  
Bit error in IQ lag 101 bit 27  
[#1395 I=0 Q=0] 07-jul-2009 18:22:56 (+3:58:46):  
Bit error in QI lag 415 bit 26  
[#1401 I=1 Q=1] 07-jul-2009 18:23:58 (+3:59:48):  
Bit error in II lag 508 bit 25  
[#1403 I=0 Q=0] 07-jul-2009 18:24:18 (+4:0:8):  
Bit error in QI lag 391 bit 27  
[#1428 I=1 Q=-1] 07-jul-2009 18:28:36 (+4:4:26):  
Bit error in II lag 425 bit 17  
[#1433 I=0 Q=1] 07-jul-2009 18:29:26 (+4:5:16):  
Bit error in II lag 34 bit 24  
[#1441 I=-1 Q=1] 07-jul-2009 18:30:48 (+4:6:38):  
Bit error in IQ lag 509 bit 25  
Bit error in QQ lag 77 bit 11  
[#1452 I=0 Q=1] 07-jul-2009 18:32:42 (+4:8:32):  
Bit error in IQ lag 17 bit 13  
[#1465 I=0 Q=0] 07-jul-2009 18:34:56 (+4:10:46):  
Bit error in QQ lag 225 bit 24  
[#1466 I=-1 Q=-1] 07-jul-2009 18:35:06 (+4:10:56):  
Bit error in IQ lag 510 bit 5  
[#1470 I=1 Q=-1] 07-jul-2009 18:35:48 (+4:11:38):  
Bit error in QI lag 507 bit 18  
[#1481 I=1 Q=-1] 07-jul-2009 18:37:40 (+4:13:30):  
Bit error in QQ lag 490 bit 11  
[#1487 I=1 Q=-1] 07-jul-2009 18:38:42 (+4:14:32):  
Bit error in IQ lag 314 bit 6  
[#1503 I=0 Q=-1] 07-jul-2009 18:41:26 (+4:17:16):  
Bit error in QI lag 299 bit 30  
[#1504 I=1 Q=1] 07-jul-2009 18:41:36 (+4:17:26):  
Bit error in II lag 343 bit 12  
[#1509 I=-1 Q=1] 07-jul-2009 18:42:28 (+4:18:18):  
Bit error in II lag 485 bit 29  
[#1511 I=1 Q=-1] 07-jul-2009 18:42:48 (+4:18:38):  
Bit error in IQ lag 390 bit 7  
[#1538 I=-1 Q=1] 07-jul-2009 18:47:26 (+4:23:16):  
Bit error in II lag 256 bit 6  
[#1542 I=1 Q=-1] 07-jul-2009 18:48:08 (+4:23:58):  
Bit error in II lag 346 bit 24  
[#1550 I=-1 Q=1] 07-jul-2009 18:49:30 (+4:25:20):  
Bit error in QI lag 233 bit 12  
[#1561 I=1 Q=-1] 07-jul-2009 18:51:24 (+4:27:14):  
Bit error in II lag 261 bit 12  
[#1569 I=1 Q=0] 07-jul-2009 18:52:46 (+4:28:36):  
Bit error in QI lag 211 bit 23  
[#1576 I=-1 Q=1] 07-jul-2009 18:53:58 (+4:29:48):  
Bit error in IQ lag 507 bit 30  
[#1577 I=0 Q=1] 07-jul-2009 18:54:08 (+4:29:58):  
Bit error in QI lag 488 bit 4  
[#1583 I=0 Q=-1] 07-jul-2009 18:55:10 (+4:30:60):  
Bit error in IQ lag 506 bit 16  
[#1584 I=1 Q=0] 07-jul-2009 18:55:20 (+4:31:10):  
Bit error in II lag 250 bit 12  
[#1586 I=-1 Q=-1] 07-jul-2009 18:55:40 (+4:31:30):  
Bit error in QQ lag 41 bit 7  
[#1601 I=-1 Q=-1] 07-jul-2009 18:58:14 (+4:34:4):  
Bit error in QQ lag 227 bit 17  
[#1611 I=0 Q=-1] 07-jul-2009 18:59:58 (+4:35:48):  
Bit error in II lag 259 bit 29  
[#1614 I=0 Q=-1] 07-jul-2009 19:00:28 (+4:36:18):  
Bit error in II lag 256 bit 24  
[#1625 I=-1 Q=-1] 07-jul-2009 19:02:22 (+4:38:12):  
Bit error in QQ lag 105 bit 12  
[#1626 I=-1 Q=-1] 07-jul-2009 19:02:32 (+4:38:22):  
Bit error in QQ lag 186 bit 25  
[#1627 I=0 Q=-1] 07-jul-2009 19:02:42 (+4:38:32):  
Bit error in QI lag 70 bit 19  
[#1630 I=-1 Q=1] 07-jul-2009 19:03:14 (+4:39:4):  
Bit error in II lag 309 bit 18  
[#1632 I=0 Q=-1] 07-jul-2009 19:03:34 (+4:39:24):  
Bit error in II lag 47 bit 22  
[#1635 I=1 Q=1] 07-jul-2009 19:04:04 (+4:39:54):  
Bit error in IQ lag 433 bit 24  
Bit error in IQ lag 498 bit 26  
Bit error in QQ lag 253 bit 17  
[#1642 I=1 Q=-1] 07-jul-2009 19:05:16 (+4:41:6):  
Bit error in II lag 479 bit 13

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 [#1660 I=1 Q=1] 07-jul-2009 19:08:22 (+4:44:12):  
 Bit error in IQ lag 263 bit 12  
 Bit error in QQ lag 30 bit 19  
 [#1668 I=1 Q=0] 07-jul-2009 19:09:44 (+4:45:34):  
 Bit error in QI lag 221 bit 5  
 [#1673 I=0 Q=1] 07-jul-2009 19:10:36 (+4:46:26):  
 Bit error in QI lag 404 bit 26  
 [#1678 I=0 Q=1] 07-jul-2009 19:11:26 (+4:47:16):  
 Bit error in IQ lag 84 bit 6  
 [#1697 I=-1 Q=-1] 07-jul-2009 19:14:42 (+4:50:32):  
 Bit error in QQ lag 208 bit 28  
 [#1699 I=-1 Q=-1] 07-jul-2009 19:15:02 (+4:50:52):  
 Bit error in QQ lag 104 bit 10  
 [#1709 I=-1 Q=-1] 07-jul-2009 19:16:46 (+4:52:36):  
 Bit error in QI lag 7 bit 4  
 [#1716 I=1 Q=1] 07-jul-2009 19:17:58 (+4:53:48):  
 Bit error in II lag 237 bit 12  
 [#1720 I=1 Q=-1] 07-jul-2009 19:18:38 (+4:54:28):  
 Bit error in II lag 210 bit 17  
 Bit error in IQ lag 498 bit 30  
 [#1723 I=0 Q=0] 07-jul-2009 19:19:10 (+4:54:60):  
 Bit error in IQ lag 479 bit 25  
 [#1738 I=0 Q=1] 07-jul-2009 19:21:44 (+4:57:34):  
 Bit error in IQ lag 267 bit 12  
 [#1739 I=1 Q=0] 07-jul-2009 19:21:54 (+4:57:44):  
 Bit error in QQ lag 300 bit 25  
 [#1742 I=1 Q=-1] 07-jul-2009 19:22:26 (+4:58:16):  
 Bit error in IQ lag 503 bit 29  
 [#1746 I=-1 Q=-1] 07-jul-2009 19:23:06 (+4:58:56):  
 Bit error in QQ lag 27 bit 2  
 [#1753 I=0 Q=0] 07-jul-2009 19:24:18 (+5:0:8):  
 Bit error in QQ lag 278 bit 28  
 [#1757 I=1 Q=1] 07-jul-2009 19:25:00 (+5:0:50):  
 Bit error in II lag 452 bit 6  
 [#1758 I=-1 Q=-1] 07-jul-2009 19:25:10 (+5:1:0):  
 Bit error in IQ lag 360 bit 28  
 [#1760 I=1 Q=1] 07-jul-2009 19:25:30 (+5:1:20):  
 Bit error in II lag 463 bit 18  
 [#1763 I=-1 Q=1] 07-jul-2009 19:26:02 (+5:1:52):  
 Bit error in QQ lag 31 bit 19  
 [#1769 I=0 Q=1] 07-jul-2009 19:27:02 (+5:2:52):  
 Bit error in QI lag 19 bit 27  
 [#1780 I=0 Q=0] 07-jul-2009 19:28:56 (+5:4:46):  
 Bit error in QQ lag 36 bit 19  
 [#1784 I=1 Q=-1] 07-jul-2009 19:29:38 (+5:5:28):  
 Bit error in II lag 1 bit 30  
 [#1793 I=-1 Q=1] 07-jul-2009 19:31:10 (+5:6:60):  
 Bit error in II lag 493 bit 18  
 [#1798 I=0 Q=-1] 07-jul-2009 19:32:02 (+5:7:52):  
 Bit error in QQ lag 117 bit 24  
 [#1812 I=0 Q=-1] 07-jul-2009 19:34:26 (+5:10:16):  
 Bit error in QQ lag 510 bit 29  
 [#1815 I=-1 Q=-1] 07-jul-2009 19:34:56 (+5:10:46):  
 Bit error in II lag 195 bit 25  
 [#1817 I=0 Q=-1] 07-jul-2009 19:35:16 (+5:11:6):  
 Bit error in II lag 296 bit 13  
 [#1823 I=-1 Q=-1] 07-jul-2009 19:36:18 (+5:12:8):  
 Bit error in IQ lag 502 bit 14  
 [#1833 I=-1 Q=-1] 07-jul-2009 19:38:02 (+5:13:52):  
 Bit error in II lag 67 bit 10  
 [#1835 I=0 Q=0] 07-jul-2009 19:38:22 (+5:14:12):  
 Bit error in QI lag 276 bit 13  
 [#1849 I=0 Q=1] 07-jul-2009 19:40:46 (+5:16:36):  
 Bit error in QI lag 85 bit 27  
 [#1854 I=1 Q=1] 07-jul-2009 19:41:38 (+5:17:28):  
 Bit error in QQ lag 237 bit 18  
 [#1862 I=1 Q=-1] 07-jul-2009 19:43:00 (+5:18:50):  
 Bit error in QI lag 105 bit 27  
 [#1864 I=1 Q=0] 07-jul-2009 19:43:20 (+5:19:10):  
 Bit error in IQ lag 41 bit 24  
 [#1879 I=-1 Q=-1] 07-jul-2009 19:45:54 (+5:21:44):  
 Bit error in QI lag 459 bit 23  
 [#1880 I=1 Q=1] 07-jul-2009 19:46:04 (+5:21:54):  
 Bit error in II lag 413 bit 18  
 [#1887 I=-1 Q=-1] 07-jul-2009 19:47:16 (+5:23:6):  
 Bit error in QI lag 215 bit 17  
 [#1889 I=0 Q=1] 07-jul-2009 19:47:36 (+5:23:26):  
 Bit error in IQ lag 504 bit 17  
 [#1899 I=1 Q=-1] 07-jul-2009 19:49:20 (+5:25:10):  
 Bit error in IQ lag 193 bit 6  
 [#1901 I=0 Q=-1] 07-jul-2009 19:49:40 (+5:25:30):  
 Bit error in QQ lag 179 bit 5  
 [#1909 I=-1 Q=-1] 07-jul-2009 19:51:02 (+5:26:52):  
 Bit error in II lag 469 bit 25
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[#1912 I=-1 Q=-1] 07-jul-2009 19:51:34 (+5:27:24):  
Bit error in II lag 238 bit 12  
Bit error in IQ lag 283 bit 18  
Bit error in QI lag 49 bit 20  
[#1921 I=0 Q=0] 07-jul-2009 19:53:06 (+5:28:56):  
Bit error in II lag 493 bit 16  
[#1924 I=0 Q=0] 07-jul-2009 19:53:36 (+5:29:26):  
Bit error in IQ lag 18 bit 23  
[#1930 I=1 Q=1] 07-jul-2009 19:54:38 (+5:30:28):  
Bit error in II lag 339 bit 12  
[#1933 I=0 Q=1] 07-jul-2009 19:55:10 (+5:30:60):  
Bit error in IQ lag 481 bit 5  
[#1955 I=-1 Q=1] 07-jul-2009 19:58:56 (+5:34:46):  
Bit error in QI lag 421 bit 2  
[#1963 I=-1 Q=1] 07-jul-2009 20:00:18 (+5:36:8):  
Bit error in QQ lag 495 bit 12  
[#1967 I=-1 Q=-1] 07-jul-2009 20:00:58 (+5:36:48):  
Bit error in QQ lag 337 bit 6  
[#1970 I=-1 Q=0] 07-jul-2009 20:01:30 (+5:37:20):  
Bit error in QQ lag 51 bit 28  
[#1976 I=1 Q=0] 07-jul-2009 20:02:32 (+5:38:22):  
Bit error in II lag 82 bit 10  
[#1984 I=0 Q=1] 07-jul-2009 20:03:54 (+5:39:44):  
Bit error in II lag 92 bit 10  
[#1996 I=0 Q=0] 07-jul-2009 20:05:58 (+5:41:48):  
Bit error in QQ lag 145 bit 30  
[#2001 I=0 Q=0] 07-jul-2009 20:06:48 (+5:42:38):  
Bit error in QI lag 449 bit 16  
[#2002 I=-1 Q=1] 07-jul-2009 20:06:58 (+5:42:48):  
Bit error in QI lag 322 bit 29  
[#2010 I=0 Q=0] 07-jul-2009 20:08:22 (+5:44:12):  
Bit error in QQ lag 45 bit 19  
[#2036 I=1 Q=1] 07-jul-2009 20:12:48 (+5:48:38):  
Bit error in II lag 213 bit 24  
[#2041 I=1 Q=1] 07-jul-2009 20:13:40 (+5:49:30):  
Bit error in II lag 162 bit 15  
Bit error in QQ lag 248 bit 16  
[#2047 I=-1 Q=1] 07-jul-2009 20:14:42 (+5:50:32):  
Bit error in QI lag 169 bit 22  
[#2055 I=1 Q=1] 07-jul-2009 20:16:04 (+5:51:54):  
Bit error in IQ lag 218 bit 6  
[#2060 I=0 Q=1] 07-jul-2009 20:16:56 (+5:52:46):  
Bit error in QI lag 391 bit 6  
[#2086 I=0 Q=1] 07-jul-2009 20:21:22 (+5:57:12):  
Bit error in QI lag 8 bit 30  
[#2090 I=0 Q=-1] 07-jul-2009 20:22:04 (+5:57:54):  
Bit error in II lag 250 bit 12  
[#2095 I=0 Q=-1] 07-jul-2009 20:22:56 (+5:58:46):  
Bit error in QQ lag 297 bit 18  
[#2113 I=0 Q=0] 07-jul-2009 20:26:00 (+6:1:50):  
Bit error in QI lag 254 bit 16  
[#2115 I=-1 Q=0] 07-jul-2009 20:26:20 (+6:2:10):  
Bit error in QI lag 237 bit 30  
[#2124 I=0 Q=1] 07-jul-2009 20:27:54 (+6:3:44):  
Bit error in IQ lag 502 bit 16  
[#2125 I=-1 Q=-1] 07-jul-2009 20:28:04 (+6:3:54):  
Bit error in II lag 195 bit 15  
[#2141 I=-1 Q=0] 07-jul-2009 20:30:48 (+6:6:38):  
Bit error in QQ lag 250 bit 25  
[#2157 I=0 Q=0] 07-jul-2009 20:33:32 (+6:9:22):  
Bit error in QQ lag 496 bit 30  
[#2164 I=-1 Q=-1] 07-jul-2009 20:34:44 (+6:10:34):  
Bit error in II lag 241 bit 13  
[#2168 I=0 Q=0] 07-jul-2009 20:35:26 (+6:11:16):  
Bit error in II lag 40 bit 16  
[#2175 I=0 Q=-1] 07-jul-2009 20:36:38 (+6:12:28):  
Bit error in QI lag 176 bit 1  
[#2176 I=-1 Q=1] 07-jul-2009 20:36:48 (+6:12:38):  
Bit error in IQ lag 485 bit 14  
[#2189 I=1 Q=1] 07-jul-2009 20:39:02 (+6:14:52):  
Bit error in QQ lag 366 bit 27  
[#2190 I=-1 Q=1] 07-jul-2009 20:39:12 (+6:15:2):  
Bit error in QI lag 240 bit 22  
[#2204 I=0 Q=1] 07-jul-2009 20:41:36 (+6:17:26):  
Bit error in II lag 398 bit 15  
[#2208 I=1 Q=0] 07-jul-2009 20:42:16 (+6:18:6):  
Bit error in II lag 246 bit 4  
[#2209 I=-1 Q=1] 07-jul-2009 20:42:26 (+6:18:16):  
Bit error in IQ lag 339 bit 11  
[#2235 I=-1 Q=0] 07-jul-2009 20:46:54 (+6:22:44):  
Bit error in IQ lag 227 bit 24  
[#2243 I=1 Q=1] 07-jul-2009 20:48:16 (+6:24:6):  
Bit error in QQ lag 494 bit 18  
[#2263 I=1 Q=0] 07-jul-2009 20:51:42 (+6:27:32):
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Bit error in IQ lag 8 bit 28  
[#2271 I=-1 Q=1] 07-jul-2009 20:53:04 (+6:28:54):  
Bit error in IQ lag 76 bit 20  
[#2274 I=1 Q=-1] 07-jul-2009 20:53:34 (+6:29:24):  
Bit error in IQ lag 26 bit 18  
[#2276 I=-1 Q=1] 07-jul-2009 20:53:56 (+6:29:46):  
Bit error in QQ lag 175 bit 13  
[#2284 I=1 Q=1] 07-jul-2009 20:55:18 (+6:31:8):  
Bit error in IQ lag 497 bit 17  
[#2286 I=1 Q=1] 07-jul-2009 20:55:38 (+6:31:28):  
Bit error in QI lag 22 bit 27  
[#2292 I=-1 Q=0] 07-jul-2009 20:56:40 (+6:32:30):  
Bit error in IQ lag 254 bit 12  
[#2299 I=0 Q=1] 07-jul-2009 20:57:52 (+6:33:42):  
Bit error in QI lag 88 bit 20  
[#2319 I=1 Q=-1] 07-jul-2009 21:01:18 (+6:37:8):  
Bit error in II lag 507 bit 0  
[#2322 I=1 Q=0] 07-jul-2009 21:01:48 (+6:37:38):  
Bit error in II lag 473 bit 13  
[#2324 I=1 Q=1] 07-jul-2009 21:02:08 (+6:37:58):  
Bit error in QQ lag 26 bit 15  
[#2329 I=-1 Q=-1] 07-jul-2009 21:03:00 (+6:38:50):  
Bit error in QQ lag 144 bit 14  
[#2338 I=-1 Q=1] 07-jul-2009 21:04:32 (+6:40:22):  
Bit error in QQ lag 502 bit 30  
[#2351 I=1 Q=1] 07-jul-2009 21:06:46 (+6:42:36):  
Bit error in IQ lag 206 bit 4  
[#2365 I=-1 Q=0] 07-jul-2009 21:09:10 (+6:45:0):  
Bit error in QQ lag 370 bit 27  
[#2376 I=0 Q=1] 07-jul-2009 21:11:04 (+6:46:54):  
Bit error in IQ lag 23 bit 30  
[#2386 I=0 Q=1] 07-jul-2009 21:12:46 (+6:48:36):  
Bit error in IQ lag 263 bit 6  
[#2392 I=1 Q=1] 07-jul-2009 21:13:48 (+6:49:38):  
Bit error in II lag 475 bit 6  
[#2400 I=0 Q=-1] 07-jul-2009 21:15:10 (+6:50:60):  
Bit error in QI lag 135 bit 22  
Bit error in QQ lag 44 bit 12  
[#2406 I=0 Q=-1] 07-jul-2009 21:16:12 (+6:52:2):  
Bit error in QI lag 285 bit 16  
[#2409 I=-1 Q=0] 07-jul-2009 21:16:42 (+6:52:32):  
Bit error in IQ lag 261 bit 4  
[#2431 I=1 Q=-1] 07-jul-2009 21:20:28 (+6:56:18):  
Bit error in QQ lag 473 bit 30  
[#2438 I=1 Q=1] 07-jul-2009 21:21:40 (+6:57:30):  
Bit error in IQ lag 257 bit 12  
[#2442 I=1 Q=-1] 07-jul-2009 21:22:22 (+6:58:12):  
Bit error in II lag 290 bit 5  
[#2452 I=-1 Q=1] 07-jul-2009 21:24:04 (+6:59:54):  
Bit error in II lag 215 bit 5  
[#2462 I=-1 Q=0] 07-jul-2009 21:25:48 (+7:1:38):  
Bit error in QQ lag 153 bit 18  
[#2468 I=-1 Q=-1] 07-jul-2009 21:26:50 (+7:2:40):  
Bit error in QQ lag 6 bit 16  
[#2471 I=1 Q=0] 07-jul-2009 21:27:20 (+7:3:10):  
Bit error in QI lag 251 bit 16  
Bit error in QQ lag 498 bit 1  
[#2477 I=1 Q=1] 07-jul-2009 21:28:22 (+7:4:12):  
Bit error in II lag 437 bit 6  
Bit error in II lag 446 bit 27  
[#2495 I=1 Q=0] 07-jul-2009 21:31:26 (+7:7:16):  
Bit error in IQ lag 117 bit 22  
[#2498 I=1 Q=-1] 07-jul-2009 21:31:58 (+7:7:48):  
Bit error in QI lag 29 bit 19  
[#2500 I=-1 Q=1] 07-jul-2009 21:32:18 (+7:8:8):  
Bit error in II lag 164 bit 19  
[#2502 I=-1 Q=-1] 07-jul-2009 21:32:38 (+7:8:28):  
Bit error in IQ lag 259 bit 25  
[#2504 I=0 Q=0] 07-jul-2009 21:33:00 (+7:8:50):  
Bit error in QQ lag 291 bit 5  
[#2506 I=-1 Q=0] 07-jul-2009 21:33:20 (+7:9:10):  
Bit error in QI lag 396 bit 23  
[#2511 I=1 Q=-1] 07-jul-2009 21:34:12 (+7:10:2):  
Bit error in QI lag 250 bit 25  
[#2524 I=-1 Q=1] 07-jul-2009 21:36:24 (+7:12:14):  
Bit error in QI lag 375 bit 9  
[#2525 I=0 Q=-1] 07-jul-2009 21:36:36 (+7:12:26):  
Bit error in QQ lag 250 bit 25  
[#2526 I=0 Q=1] 07-jul-2009 21:36:46 (+7:12:36):  
Bit error in IQ lag 297 bit 28  
[#2527 I=0 Q=0] 07-jul-2009 21:36:56 (+7:12:46):  
Bit error in II lag 311 bit 24  
[#2543 I=1 Q=-1] 07-jul-2009 21:39:40 (+7:15:30):  
Bit error in II lag 406 bit 27



[#2545 I=1 Q=1] 07-jul-2009 21:40:00 (+7:15:50):  
Bit error in IQ lag 444 bit 28  
[#2550 I=-1 Q=-1] 07-jul-2009 21:40:52 (+7:16:42):  
Bit error in QQ lag 340 bit 12  
[#2552 I=1 Q=1] 07-jul-2009 21:41:12 (+7:17:2):  
Bit error in QI lag 19 bit 12  
[#2553 I=1 Q=1] 07-jul-2009 21:41:24 (+7:17:14):  
Bit error in II lag 485 bit 29  
[#2557 I=1 Q=-1] 07-jul-2009 21:42:04 (+7:17:54):  
Bit error in QQ lag 212 bit 15  
[#2559 I=-1 Q=-1] 07-jul-2009 21:42:24 (+7:18:14):  
Bit error in IQ lag 33 bit 23  
[#2567 I=-1 Q=0] 07-jul-2009 21:43:48 (+7:19:38):  
Bit error in II lag 270 bit 29  
[#2571 I=-1 Q=-1] 07-jul-2009 21:44:28 (+7:20:18):  
Bit error in II lag 433 bit 30  
[#2575 I=0 Q=0] 07-jul-2009 21:45:10 (+7:20:60):  
Bit error in QI lag 232 bit 4  
[#2585 I=0 Q=1] 07-jul-2009 21:46:52 (+7:22:42):  
Bit error in II lag 289 bit 13  
Bit error in II lag 413 bit 28  
[#2593 I=-1 Q=-1] 07-jul-2009 21:48:14 (+7:24:4):  
Bit error in QI lag 281 bit 12  
[#2595 I=0 Q=-1] 07-jul-2009 21:48:36 (+7:24:26):  
Bit error in QQ lag 178 bit 5  
[#2600 I=0 Q=1] 07-jul-2009 21:49:26 (+7:25:16):  
Bit error in QQ lag 3 bit 24  
[#2616 I=0 Q=1] 07-jul-2009 21:52:10 (+7:27:60):  
Bit error in IQ lag 484 bit 28  
[#2626 I=0 Q=-1] 07-jul-2009 21:53:54 (+7:29:44):  
Bit error in II lag 28 bit 11  
[#2627 I=-1 Q=0] 07-jul-2009 21:54:04 (+7:29:54):  
Bit error in IQ lag 498 bit 29  
[#2640 I=-1 Q=0] 07-jul-2009 21:56:18 (+7:32:8):  
Bit error in QI lag 198 bit 16  
Bit error in QQ lag 278 bit 16  
[#2655 I=0 Q=1] 07-jul-2009 21:58:52 (+7:34:42):  
Bit error in II lag 149 bit 27  
[#2677 I=1 Q=1] 07-jul-2009 22:02:38 (+7:38:28):  
Bit error in II lag 262 bit 6  
[#2682 I=0 Q=-1] 07-jul-2009 22:03:30 (+7:39:20):  
Bit error in IQ lag 225 bit 14  
[#2692 I=0 Q=-1] 07-jul-2009 22:05:12 (+7:41:2):  
Bit error in II lag 378 bit 1  
[#2708 I=-1 Q=-1] 07-jul-2009 22:07:56 (+7:43:46):  
Bit error in II lag 22 bit 26  
Bit error in QQ lag 315 bit 12  
[#2729 I=-1 Q=1] 07-jul-2009 22:11:32 (+7:47:22):  
Bit error in QQ lag 274 bit 5  
[#2732 I=-1 Q=0] 07-jul-2009 22:12:04 (+7:47:54):  
Bit error in QQ lag 24 bit 6  
[#2761 I=1 Q=-1] 07-jul-2009 22:17:02 (+7:52:52):  
Bit error in II lag 416 bit 18  
[#2769 I=0 Q=-1] 07-jul-2009 22:18:24 (+7:54:14):  
Bit error in II lag 498 bit 25  
[#2781 I=-1 Q=0] 07-jul-2009 22:20:26 (+7:56:16):  
Bit error in II lag 328 bit 25  
Bit error in QQ lag 432 bit 25  
[#2784 I=0 Q=0] 07-jul-2009 22:20:58 (+7:56:48):  
Bit error in QI lag 96 bit 15  
[#2785 I=0 Q=-1] 07-jul-2009 22:21:08 (+7:56:58):  
Bit error in QI lag 249 bit 5  
[#2810 I=-1 Q=-1] 07-jul-2009 22:25:24 (+8:1:14):  
Bit error in II lag 249 bit 17  
[#2811 I=0 Q=1] 07-jul-2009 22:25:34 (+8:1:24):  
Bit error in QI lag 286 bit 16  
[#2813 I=0 Q=1] 07-jul-2009 22:25:56 (+8:1:46):  
Bit error in II lag 278 bit 16  
[#2816 I=1 Q=1] 07-jul-2009 22:26:26 (+8:2:16):  
Bit error in QI lag 306 bit 18  
[#2827 I=1 Q=-1] 07-jul-2009 22:28:20 (+8:4:10):  
Bit error in QI lag 354 bit 28  
[#2836 I=-1 Q=0] 07-jul-2009 22:29:52 (+8:5:42):  
Bit error in QQ lag 422 bit 11  
[#2844 I=1 Q=1] 07-jul-2009 22:31:14 (+8:7:4):  
Bit error in IQ lag 502 bit 12  
[#2852 I=1 Q=-1] 07-jul-2009 22:32:36 (+8:8:26):  
Bit error in IQ lag 268 bit 29  
[#2854 I=1 Q=0] 07-jul-2009 22:32:58 (+8:8:48):  
Bit error in QQ lag 241 bit 29  
[#2856 I=1 Q=1] 07-jul-2009 22:33:18 (+8:9:8):  
Bit error in QQ lag 443 bit 27  
[#2857 I=-1 Q=1] 07-jul-2009 22:33:28 (+8:9:18):  
Bit error in IQ lag 446 bit 29

[#2859 I=1 Q=-1] 07-jul-2009 22:33:48 (+8:9:38):  
Bit error in QI lag 253 bit 5  
Bit error in QQ lag 75 bit 27  
[#2865 I=0 Q=-1] 07-jul-2009 22:34:50 (+8:10:40):  
Bit error in IQ lag 495 bit 6  
[#2871 I=1 Q=1] 07-jul-2009 22:35:52 (+8:11:42):  
Bit error in IQ lag 16 bit 25  
[#2874 I=1 Q=1] 07-jul-2009 22:36:22 (+8:12:12):  
Bit error in QQ lag 499 bit 24  
[#2881 I=-1 Q=1] 07-jul-2009 22:37:34 (+8:13:24):  
Bit error in IQ lag 321 bit 24  
[#2883 I=-1 Q=0] 07-jul-2009 22:37:56 (+8:13:46):  
Bit error in QQ lag 341 bit 24  
[#2903 I=1 Q=-1] 07-jul-2009 22:41:20 (+8:17:10):  
Bit error in IQ lag 294 bit 25  
[#2917 I=0 Q=-1] 07-jul-2009 22:43:44 (+8:19:34):  
Bit error in QI lag 250 bit 18  
[#2929 I=0 Q=1] 07-jul-2009 22:45:48 (+8:21:38):  
Bit error in II lag 265 bit 30  
[#2932 I=0 Q=1] 07-jul-2009 22:46:18 (+8:22:8):  
Bit error in QI lag 232 bit 13  
[#2943 I=1 Q=0] 07-jul-2009 22:48:12 (+8:24:2):  
Bit error in QQ lag 491 bit 30  
[#2977 I=-1 Q=1] 07-jul-2009 22:54:02 (+8:29:52):  
Bit error in QQ lag 278 bit 12  
[#2980 I=-1 Q=0] 07-jul-2009 22:54:32 (+8:30:22):  
Bit error in QQ lag 506 bit 24  
[#2991 I=-1 Q=0] 07-jul-2009 22:56:26 (+8:32:16):  
Bit error in IQ lag 364 bit 23  
[#2996 I=1 Q=0] 07-jul-2009 22:57:16 (+8:33:6):  
Bit error in QQ lag 225 bit 30  
[#2997 I=1 Q=0] 07-jul-2009 22:57:26 (+8:33:16):  
Bit error in QI lag 189 bit 10  
[#3003 I=-1 Q=-1] 07-jul-2009 22:58:28 (+8:34:18):  
Bit error in II lag 484 bit 25  
[#3005 I=1 Q=-1] 07-jul-2009 22:58:50 (+8:34:40):  
Bit error in QI lag 500 bit 16  
[#3008 I=-1 Q=0] 07-jul-2009 22:59:20 (+8:35:10):  
Bit error in QQ lag 176 bit 5  
[#3011 I=-1 Q=1] 07-jul-2009 22:59:50 (+8:35:40):  
Bit error in IQ lag 258 bit 13  
[#3016 I=-1 Q=-1] 07-jul-2009 23:00:42 (+8:36:32):  
Bit error in IQ lag 225 bit 12  
[#3025 I=-1 Q=0] 07-jul-2009 23:02:14 (+8:38:4):  
Bit error in QI lag 191 bit 13  
[#3033 I=-1 Q=-1] 07-jul-2009 23:03:38 (+8:39:28):  
Bit error in QQ lag 323 bit 1  
[#3035 I=1 Q=1] 07-jul-2009 23:03:58 (+8:39:48):  
Bit error in IQ lag 469 bit 13  
[#3038 I=-1 Q=1] 07-jul-2009 23:04:28 (+8:40:18):  
Bit error in IQ lag 487 bit 6  
[#3052 I=-1 Q=1] 07-jul-2009 23:06:52 (+8:42:42):  
Bit error in QI lag 112 bit 16  
[#3055 I=-1 Q=1] 07-jul-2009 23:07:24 (+8:43:14):  
Bit error in QQ lag 326 bit 1  
[#3056 I=1 Q=0] 07-jul-2009 23:07:34 (+8:43:24):  
Bit error in II lag 294 bit 6  
[#3057 I=1 Q=-1] 07-jul-2009 23:07:44 (+8:43:34):  
Bit error in IQ lag 490 bit 29  
[#3059 I=1 Q=1] 07-jul-2009 23:08:04 (+8:43:54):  
Bit error in QI lag 283 bit 2  
[#3072 I=-1 Q=-1] 07-jul-2009 23:10:18 (+8:46:8):  
Bit error in QI lag 489 bit 25  
[#3073 I=0 Q=1] 07-jul-2009 23:10:28 (+8:46:18):  
Bit error in IQ lag 436 bit 5  
[#3075 I=1 Q=1] 07-jul-2009 23:10:50 (+8:46:40):  
Bit error in IQ lag 504 bit 25  
[#3081 I=1 Q=-1] 07-jul-2009 23:11:50 (+8:47:40):  
Bit error in II lag 82 bit 15  
[#3083 I=0 Q=-1] 07-jul-2009 23:12:12 (+8:48:2):  
Bit error in IQ lag 209 bit 12  
[#3086 I=0 Q=0] 07-jul-2009 23:12:42 (+8:48:32):  
Bit error in IQ lag 490 bit 25  
[#3102 I=0 Q=1] 07-jul-2009 23:15:26 (+8:51:16):  
Bit error in QI lag 90 bit 7  
[#3116 I=1 Q=-1] 07-jul-2009 23:17:50 (+8:53:40):  
Bit error in II lag 328 bit 29  
[#3121 I=1 Q=-1] 07-jul-2009 23:18:42 (+8:54:32):  
Bit error in QI lag 131 bit 20  
[#3122 I=-1 Q=0] 07-jul-2009 23:18:52 (+8:54:42):  
Bit error in QI lag 308 bit 17  
[#3126 I=0 Q=1] 07-jul-2009 23:19:34 (+8:55:24):  
Bit error in II lag 61 bit 4  
[#3129 I=1 Q=1] 07-jul-2009 23:20:04 (+8:55:54):

Bit error in QI lag 411 bit 25  
[#3131 I=1 Q=1] 07-jul-2009 23:20:24 (+8:56:14):  
Bit error in IQ lag 283 bit 12  
[#3135 I=0 Q=0] 07-jul-2009 23:21:06 (+8:56:56):  
Bit error in IQ lag 70 bit 26  
[#3140 I=-1 Q=-1] 07-jul-2009 23:21:58 (+8:57:48):  
Bit error in II lag 476 bit 14  
[#3163 I=1 Q=-1] 07-jul-2009 23:25:54 (+9:1:44):  
Bit error in II lag 495 bit 14  
Bit error in QI lag 245 bit 4  
[#3166 I=0 Q=0] 07-jul-2009 23:26:24 (+9:2:14):  
Bit error in IQ lag 278 bit 12  
[#3170 I=1 Q=1] 07-jul-2009 23:27:06 (+9:2:56):  
Bit error in II lag 258 bit 24  
[#3173 I=0 Q=1] 07-jul-2009 23:27:36 (+9:3:26):  
Bit error in QI lag 213 bit 16  
[#3178 I=0 Q=-1] 07-jul-2009 23:28:28 (+9:4:18):  
Bit error in II lag 289 bit 29  
Bit error in IQ lag 273 bit 18  
[#3216 I=0 Q=0] 07-jul-2009 23:34:58 (+9:10:48):  
Bit error in QI lag 457 bit 29  
[#3223 I=1 Q=0] 07-jul-2009 23:36:10 (+9:11:60):  
Bit error in QQ lag 462 bit 13  
[#3255 I=1 Q=-1] 07-jul-2009 23:41:40 (+9:17:30):  
Bit error in QQ lag 60 bit 16  
[#3268 I=1 Q=1] 07-jul-2009 23:43:54 (+9:19:44):  
Bit error in II lag 178 bit 24  
[#3275 I=-1 Q=-1] 07-jul-2009 23:45:06 (+9:20:56):  
Bit error in QQ lag 250 bit 30  
[#3289 I=-1 Q=1] 07-jul-2009 23:47:30 (+9:23:20):  
Bit error in II lag 9 bit 25  
[#3290 I=0 Q=0] 07-jul-2009 23:47:40 (+9:23:30):  
Bit error in QI lag 248 bit 17  
[#3293 I=0 Q=0] 07-jul-2009 23:48:10 (+9:23:60):  
Bit error in QI lag 494 bit 11  
[#3294 I=1 Q=-1] 07-jul-2009 23:48:20 (+9:24:10):  
Bit error in QQ lag 113 bit 28  
[#3297 I=-1 Q=-1] 07-jul-2009 23:48:52 (+9:24:42):  
Bit error in IQ lag 257 bit 29  
[#3298 I=0 Q=1] 07-jul-2009 23:49:02 (+9:24:52):  
Bit error in II lag 136 bit 19  
[#3303 I=0 Q=0] 07-jul-2009 23:49:54 (+9:25:44):  
Bit error in IQ lag 444 bit 14  
[#3315 I=1 Q=-1] 07-jul-2009 23:51:56 (+9:27:46):  
Bit error in II lag 7 bit 29  
[#3317 I=1 Q=-1] 07-jul-2009 23:52:16 (+9:28:6):  
Bit error in QI lag 265 bit 2  
[#3338 I=-1 Q=1] 07-jul-2009 23:55:52 (+9:31:42):  
Bit error in QI lag 459 bit 30  
[#3339 I=-1 Q=-1] 07-jul-2009 23:56:02 (+9:31:52):  
Bit error in II lag 9 bit 15  
Bit error in II lag 290 bit 17  
[#3340 I=-1 Q=1] 07-jul-2009 23:56:14 (+9:32:4):  
Bit error in IQ lag 44 bit 17  
[#3343 I=1 Q=0] 07-jul-2009 23:56:44 (+9:32:34):  
Bit error in II lag 510 bit 4  
[#3386 I=-1 Q=-1] 08-jul-2009 00:04:06 (+9:39:56):  
Bit error in IQ lag 65 bit 27  
[#3392 I=1 Q=0] 08-jul-2009 00:05:08 (+9:40:58):  
Bit error in QQ lag 281 bit 25  
[#3395 I=1 Q=0] 08-jul-2009 00:05:38 (+9:41:28):  
Bit error in QQ lag 164 bit 29  
[#3406 I=1 Q=-1] 08-jul-2009 00:07:32 (+9:43:22):  
Bit error in QQ lag 319 bit 25  
[#3408 I=0 Q=-1] 08-jul-2009 00:07:52 (+9:43:42):  
Bit error in QI lag 236 bit 25  
[#3412 I=-1 Q=0] 08-jul-2009 00:08:34 (+9:44:24):  
Bit error in QI lag 220 bit 15  
[#3413 I=-1 Q=1] 08-jul-2009 00:08:44 (+9:44:34):  
Bit error in QI lag 293 bit 30  
[#3417 I=-1 Q=1] 08-jul-2009 00:09:24 (+9:45:14):  
Bit error in II lag 337 bit 0  
[#3430 I=-1 Q=-1] 08-jul-2009 00:11:38 (+9:47:28):  
Bit error in QI lag 30 bit 17  
[#3434 I=-1 Q=-1] 08-jul-2009 00:12:20 (+9:48:10):  
Bit error in IQ lag 329 bit 30  
[#3435 I=1 Q=-1] 08-jul-2009 00:12:30 (+9:48:20):  
Bit error in II lag 503 bit 29  
[#3448 I=1 Q=-1] 08-jul-2009 00:14:44 (+9:50:34):  
Bit error in QI lag 289 bit 4  
[#3449 I=0 Q=0] 08-jul-2009 00:14:54 (+9:50:44):  
Bit error in II lag 279 bit 24  
[#3451 I=0 Q=-1] 08-jul-2009 00:15:14 (+9:51:4):  
Bit error in IQ lag 120 bit 22

[#3460 I=0 Q=0] 08-jul-2009 00:16:46 (+9:52:36):  
Bit error in QI lag 259 bit 24  
[#3462 I=-1 Q=0] 08-jul-2009 00:17:08 (+9:52:58):  
Bit error in QQ lag 487 bit 13  
[#3465 I=0 Q=0] 08-jul-2009 00:17:38 (+9:53:28):  
Bit error in II lag 332 bit 30  
Bit error in QQ lag 76 bit 16  
[#3475 I=0 Q=1] 08-jul-2009 00:19:22 (+9:55:12):  
Bit error in II lag 200 bit 26  
[#3487 I=0 Q=1] 08-jul-2009 00:21:24 (+9:57:14):  
Bit error in IQ lag 202 bit 6  
[#3488 I=1 Q=1] 08-jul-2009 00:21:34 (+9:57:24):  
Bit error in QI lag 217 bit 28  
[#3497 I=1 Q=1] 08-jul-2009 00:23:08 (+9:58:58):  
Bit error in II lag 485 bit 30  
[#3503 I=0 Q=-1] 08-jul-2009 00:24:08 (+9:59:58):  
Bit error in IQ lag 245 bit 12  
[#3510 I=-1 Q=0] 08-jul-2009 00:25:20 (+10:1:10):  
Bit error in IQ lag 43 bit 4  
[#3515 I=0 Q=1] 08-jul-2009 00:26:12 (+10:2:2):  
Bit error in QI lag 192 bit 16  
[#3535 I=-1 Q=-1] 08-jul-2009 00:29:38 (+10:5:28):  
Bit error in QQ lag 83 bit 9  
[#3537 I=-1 Q=1] 08-jul-2009 00:29:58 (+10:5:48):  
Bit error in QQ lag 448 bit 2  
[#3542 I=-1 Q=1] 08-jul-2009 00:30:50 (+10:6:40):  
Bit error in II lag 383 bit 19  
[#3544 I=-1 Q=-1] 08-jul-2009 00:31:10 (+10:6:60):  
Bit error in QQ lag 184 bit 10  
[#3546 I=1 Q=-1] 08-jul-2009 00:31:30 (+10:7:20):  
Bit error in QQ lag 209 bit 13  
[#3551 I=0 Q=0] 08-jul-2009 00:32:22 (+10:8:12):  
Bit error in QI lag 258 bit 6  
[#3554 I=-1 Q=-1] 08-jul-2009 00:32:54 (+10:8:44):  
Bit error in QQ lag 295 bit 12  
[#3563 I=1 Q=0] 08-jul-2009 00:34:26 (+10:10:16):  
Bit error in QI lag 7 bit 24  
[#3574 I=-1 Q=1] 08-jul-2009 00:36:18 (+10:12:8):  
Bit error in QQ lag 469 bit 24  
[#3583 I=0 Q=-1] 08-jul-2009 00:37:52 (+10:13:42):  
Bit error in IQ lag 71 bit 21  
[#3586 I=0 Q=-1] 08-jul-2009 00:38:22 (+10:14:12):  
Bit error in II lag 316 bit 4  
[#3597 I=-1 Q=1] 08-jul-2009 00:40:16 (+10:16:6):  
Bit error in IQ lag 0 bit 12  
[#3598 I=-1 Q=0] 08-jul-2009 00:40:26 (+10:16:16):  
Bit error in QQ lag 217 bit 15  
[#3603 I=1 Q=-1] 08-jul-2009 00:41:16 (+10:17:6):  
Bit error in IQ lag 495 bit 18  
[#3611 I=-1 Q=-1] 08-jul-2009 00:42:40 (+10:18:30):  
Bit error in QQ lag 197 bit 16  
[#3612 I=1 Q=-1] 08-jul-2009 00:42:50 (+10:18:40):  
Bit error in QI lag 81 bit 28  
[#3635 I=0 Q=1] 08-jul-2009 00:46:46 (+10:22:36):  
Bit error in II lag 405 bit 24  
[#3637 I=1 Q=-1] 08-jul-2009 00:47:06 (+10:22:56):  
Bit error in II lag 262 bit 17  
[#3643 I=1 Q=0] 08-jul-2009 00:48:08 (+10:23:58):  
Bit error in IQ lag 436 bit 4  
[#3652 I=1 Q=0] 08-jul-2009 00:49:40 (+10:25:30):  
Bit error in QQ lag 331 bit 15  
[#3656 I=-1 Q=-1] 08-jul-2009 00:50:22 (+10:26:12):  
Bit error in IQ lag 299 bit 18  
[#3673 I=1 Q=-1] 08-jul-2009 00:53:16 (+10:29:6):  
Bit error in II lag 106 bit 30  
[#3674 I=0 Q=0] 08-jul-2009 00:53:26 (+10:29:16):  
Bit error in II lag 78 bit 10  
[#3689 I=1 Q=1] 08-jul-2009 00:56:00 (+10:31:50):  
Bit error in QI lag 228 bit 2  
[#3691 I=0 Q=-1] 08-jul-2009 00:56:22 (+10:32:12):  
Bit error in QQ lag 483 bit 29  
[#3697 I=0 Q=1] 08-jul-2009 00:57:22 (+10:33:12):  
Bit error in QI lag 244 bit 27  
[#3698 I=1 Q=1] 08-jul-2009 00:57:34 (+10:33:24):  
Bit error in II lag 477 bit 30  
[#3708 I=-1 Q=-1] 08-jul-2009 00:59:16 (+10:35:6):  
Bit error in QQ lag 261 bit 28  
[#3712 I=1 Q=-1] 08-jul-2009 00:59:58 (+10:35:48):  
Bit error in II lag 42 bit 17  
[#3716 I=1 Q=-1] 08-jul-2009 01:00:38 (+10:36:28):  
Bit error in QQ lag 478 bit 13  
[#3718 I=0 Q=-1] 08-jul-2009 01:00:58 (+10:36:48):  
Bit error in IQ lag 416 bit 12  
[#3720 I=-1 Q=1] 08-jul-2009 01:01:20 (+10:37:10):

Bit error in II lag 249 bit 18  
[#3722 I=-1 Q=1] 08-jul-2009 01:01:40 (+10:37:30):  
Bit error in QI lag 274 bit 16  
[#3728 I=1 Q=0] 08-jul-2009 01:02:42 (+10:38:32):  
Bit error in II lag 1 bit 1  
[#3731 I=1 Q=0] 08-jul-2009 01:03:12 (+10:39:2):  
Bit error in QI lag 269 bit 28  
[#3734 I=0 Q=-1] 08-jul-2009 01:03:44 (+10:39:34):  
Bit error in QQ lag 298 bit 5  
[#3736 I=1 Q=1] 08-jul-2009 01:04:04 (+10:39:54):  
Bit error in QQ lag 6 bit 27  
[#3745 I=1 Q=1] 08-jul-2009 01:05:36 (+10:41:26):  
Bit error in QQ lag 289 bit 4  
[#3758 I=-1 Q=-1] 08-jul-2009 01:07:50 (+10:43:40):  
Bit error in II lag 292 bit 11  
[#3760 I=1 Q=1] 08-jul-2009 01:08:10 (+10:44:0):  
Bit error in IQ lag 372 bit 15  
Bit error in QI lag 382 bit 19  
[#3763 I=0 Q=1] 08-jul-2009 01:08:42 (+10:44:32):  
Bit error in QI lag 241 bit 17  
[#3771 I=0 Q=1] 08-jul-2009 01:10:04 (+10:45:54):  
Bit error in QQ lag 271 bit 16  
[#3775 I=-1 Q=0] 08-jul-2009 01:10:44 (+10:46:34):  
Bit error in QI lag 86 bit 19  
[#3780 I=-1 Q=0] 08-jul-2009 01:11:36 (+10:47:26):  
Bit error in II lag 98 bit 27  
Bit error in QI lag 237 bit 15  
[#3783 I=-1 Q=0] 08-jul-2009 01:12:08 (+10:47:58):  
Bit error in QQ lag 238 bit 5  
[#3793 I=0 Q=0] 08-jul-2009 01:13:50 (+10:49:40):  
Bit error in II lag 81 bit 15  
Bit error in QQ lag 240 bit 30  
[#3794 I=0 Q=1] 08-jul-2009 01:14:00 (+10:49:50):  
Bit error in QI lag 265 bit 16  
[#3797 I=1 Q=-1] 08-jul-2009 01:14:32 (+10:50:22):  
Bit error in QI lag 300 bit 30  
[#3802 I=-1 Q=1] 08-jul-2009 01:15:22 (+10:51:12):  
Bit error in QQ lag 244 bit 17  
[#3806 I=1 Q=1] 08-jul-2009 01:16:04 (+10:51:54):  
Bit error in IQ lag 164 bit 4  
[#3807 I=1 Q=-1] 08-jul-2009 01:16:14 (+10:52:4):  
Bit error in IQ lag 280 bit 21  
[#3810 I=0 Q=1] 08-jul-2009 01:16:44 (+10:52:34):  
Bit error in QI lag 255 bit 17  
[#3823 I=0 Q=0] 08-jul-2009 01:18:58 (+10:54:48):  
Bit error in QQ lag 278 bit 5  
[#3834 I=-1 Q=0] 08-jul-2009 01:20:52 (+10:56:42):  
Bit error in QQ lag 310 bit 29  
[#3854 I=0 Q=1] 08-jul-2009 01:24:16 (+11:0:6):  
Bit error in QI lag 235 bit 27  
[#3875 I=-1 Q=-1] 08-jul-2009 01:27:52 (+11:3:42):  
Bit error in IQ lag 489 bit 24  
[#3882 I=1 Q=1] 08-jul-2009 01:29:04 (+11:4:54):  
Bit error in IQ lag 80 bit 27  
[#3888 I=1 Q=1] 08-jul-2009 01:30:06 (+11:5:56):  
Bit error in II lag 36 bit 18  
[#3895 I=0 Q=-1] 08-jul-2009 01:31:18 (+11:7:8):  
Bit error in II lag 29 bit 24  
[#3897 I=1 Q=0] 08-jul-2009 01:31:38 (+11:7:28):  
Bit error in QQ lag 40 bit 16  
[#3899 I=-1 Q=0] 08-jul-2009 01:32:00 (+11:7:50):  
Bit error in QI lag 261 bit 26  
[#3909 I=-1 Q=1] 08-jul-2009 01:33:42 (+11:9:32):  
Bit error in QQ lag 73 bit 28  
[#3916 I=0 Q=1] 08-jul-2009 01:34:54 (+11:10:44):  
Bit error in IQ lag 472 bit 12  
[#3919 I=0 Q=0] 08-jul-2009 01:35:24 (+11:11:14):  
Bit error in IQ lag 78 bit 19  
[#3920 I=1 Q=1] 08-jul-2009 01:35:36 (+11:11:26):  
Bit error in IQ lag 70 bit 23  
[#3921 I=0 Q=1] 08-jul-2009 01:35:46 (+11:11:36):  
Bit error in QI lag 385 bit 15  
[#3939 I=1 Q=1] 08-jul-2009 01:38:50 (+11:14:40):  
Bit error in IQ lag 434 bit 4  
[#3951 I=-1 Q=1] 08-jul-2009 01:40:54 (+11:16:44):  
Bit error in IQ lag 277 bit 16  
[#3952 I=-1 Q=0] 08-jul-2009 01:41:04 (+11:16:54):  
Bit error in II lag 466 bit 16  
[#3960 I=0 Q=-1] 08-jul-2009 01:42:26 (+11:18:16):  
Bit error in II lag 245 bit 24  
[#3961 I=1 Q=-1] 08-jul-2009 01:42:36 (+11:18:26):  
Bit error in QI lag 269 bit 25  
[#3973 I=1 Q=-1] 08-jul-2009 01:44:40 (+11:20:30):  
Bit error in II lag 144 bit 28

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[#3981 I=0 Q=0] 08-jul-2009 01:46:02 (+11:21:52):  
Bit error in IQ lag 213 bit 18  
[#3986 I=1 Q=1] 08-jul-2009 01:46:54 (+11:22:44):  
Bit error in IQ lag 361 bit 15  
[#3989 I=1 Q=0] 08-jul-2009 01:47:24 (+11:23:14):  
Bit error in IQ lag 506 bit 11  
Bit error in QQ lag 24 bit 16  
[#3992 I=1 Q=1] 08-jul-2009 01:47:56 (+11:23:46):  
Bit error in IQ lag 328 bit 17  
Bit error in IQ lag 462 bit 4  
[#3995 I=-1 Q=1] 08-jul-2009 01:48:26 (+11:24:16):  
Bit error in IQ lag 194 bit 4  
[#3998 I=1 Q=0] 08-jul-2009 01:48:56 (+11:24:46):  
Bit error in QI lag 50 bit 7  
[#4000 I=-1 Q=1] 08-jul-2009 01:49:18 (+11:25:8):  
Bit error in II lag 462 bit 13  
[#4001 I=1 Q=-1] 08-jul-2009 01:49:28 (+11:25:18):  
Bit error in II lag 276 bit 18  
[#4007 I=-1 Q=0] 08-jul-2009 01:50:30 (+11:26:20):  
Bit error in QQ lag 212 bit 12  
[#4008 I=1 Q=-1] 08-jul-2009 01:50:40 (+11:26:30):  
Bit error in II lag 67 bit 15  
[#4010 I=-1 Q=-1] 08-jul-2009 01:51:00 (+11:26:50):  
Bit error in QQ lag 149 bit 22  
[#4033 I=0 Q=1] 08-jul-2009 01:54:56 (+11:30:46):  
Bit error in QQ lag 126 bit 16  
[#4038 I=0 Q=-1] 08-jul-2009 01:55:48 (+11:31:38):  
Bit error in QI lag 219 bit 30  
[#4040 I=-1 Q=1] 08-jul-2009 01:56:08 (+11:31:58):  
Bit error in QQ lag 225 bit 30  
Bit error in QQ lag 315 bit 25  
[#4062 I=0 Q=0] 08-jul-2009 01:59:54 (+11:35:44):  
Bit error in QI lag 272 bit 28  
[#4065 I=-1 Q=1] 08-jul-2009 02:00:26 (+11:36:16):  
Bit error in IQ lag 403 bit 19  
[#4068 I=1 Q=0] 08-jul-2009 02:00:56 (+11:36:46):  
Bit error in QQ lag 504 bit 14  
[#4070 I=-1 Q=-1] 08-jul-2009 02:01:16 (+11:37:6):  
Bit error in QQ lag 266 bit 28  
[#4077 I=-1 Q=-1] 08-jul-2009 02:02:28 (+11:38:18):  
Bit error in QQ lag 449 bit 29  
[#4081 I=-1 Q=-1] 08-jul-2009 02:03:10 (+11:38:60):  
Bit error in QI lag 150 bit 30  
[#4085 I=1 Q=-1] 08-jul-2009 02:03:50 (+11:39:40):  
Bit error in QQ lag 256 bit 18  
[#4097 I=1 Q=-1] 08-jul-2009 02:05:54 (+11:41:44):  
Bit error in II lag 97 bit 27  
[#4114 I=1 Q=1] 08-jul-2009 02:08:48 (+11:44:38):  
Bit error in II lag 505 bit 26  
Bit error in IQ lag 3 bit 12  
[#4115 I=0 Q=-1] 08-jul-2009 02:08:58 (+11:44:48):  
Bit error in IQ lag 460 bit 26  
[#4121 I=1 Q=-1] 08-jul-2009 02:10:00 (+11:45:50):  
Bit error in QI lag 276 bit 26  
[#4126 I=0 Q=-1] 08-jul-2009 02:10:52 (+11:46:42):  
Bit error in II lag 267 bit 17  
[#4141 I=0 Q=0] 08-jul-2009 02:13:26 (+11:49:16):  
Bit error in II lag 290 bit 26  
[#4143 I=-1 Q=-1] 08-jul-2009 02:13:46 (+11:49:36):  
Bit error in IQ lag 283 bit 30  
Bit error in QQ lag 309 bit 28  
[#4148 I=1 Q=1] 08-jul-2009 02:14:38 (+11:50:28):  
Bit error in QQ lag 326 bit 30  
[#4152 I=-1 Q=0] 08-jul-2009 02:15:20 (+11:51:10):  
Bit error in II lag 16 bit 29  
[#4177 I=-1 Q=-1] 08-jul-2009 02:19:36 (+11:55:26):  
Bit error in II lag 64 bit 23  
[#4182 I=0 Q=1] 08-jul-2009 02:20:28 (+11:56:18):  
Bit error in QQ lag 437 bit 16  
[#4184 I=1 Q=0] 08-jul-2009 02:20:48 (+11:56:38):  
Bit error in II lag 379 bit 11  
[#4198 I=0 Q=0] 08-jul-2009 02:23:12 (+11:59:2):  
Bit error in QI lag 277 bit 24  
[#4210 I=1 Q=1] 08-jul-2009 02:25:14 (+12:1:4):  
Bit error in QQ lag 298 bit 28  
[#4212 I=1 Q=1] 08-jul-2009 02:25:36 (+12:1:26):  
Bit error in QQ lag 244 bit 29  
[#4218 I=-1 Q=1] 08-jul-2009 02:26:38 (+12:2:28):  
Bit error in IQ lag 228 bit 17  
[#4219 I=-1 Q=-1] 08-jul-2009 02:26:48 (+12:2:38):  
Bit error in QQ lag 135 bit 15  
[#4222 I=1 Q=0] 08-jul-2009 02:27:18 (+12:3:8):  
Bit error in QI lag 313 bit 11  
Bit error in QQ lag 460 bit 25
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[#4230 I=1 Q=-1] 08-jul-2009 02:28:40 (+12:4:30):  
Bit error in QQ lag 210 bit 30  
[#4235 I=1 Q=0] 08-jul-2009 02:29:32 (+12:5:22):  
Bit error in QQ lag 233 bit 15  
[#4240 I=0 Q=1] 08-jul-2009 02:30:24 (+12:6:14):  
Bit error in II lag 96 bit 16  
[#4257 I=1 Q=-1] 08-jul-2009 02:33:18 (+12:9:8):  
Bit error in II lag 343 bit 16  
[#4278 I=0 Q=0] 08-jul-2009 02:36:54 (+12:12:44):  
Bit error in QI lag 111 bit 6  
[#4281 I=-1 Q=1] 08-jul-2009 02:37:24 (+12:13:14):  
Bit error in IQ lag 136 bit 8  
[#4290 I=0 Q=-1] 08-jul-2009 02:38:56 (+12:14:46):  
Bit error in II lag 18 bit 15  
[#4294 I=1 Q=1] 08-jul-2009 02:39:38 (+12:15:28):  
Bit error in QQ lag 313 bit 13  
[#4302 I=0 Q=-1] 08-jul-2009 02:41:00 (+12:16:50):  
Bit error in II lag 21 bit 11  
[#4304 I=0 Q=1] 08-jul-2009 02:41:20 (+12:17:10):  
Bit error in QI lag 217 bit 4  
[#4326 I=0 Q=-1] 08-jul-2009 02:45:06 (+12:20:56):  
Bit error in II lag 459 bit 30  
[#4330 I=-1 Q=0] 08-jul-2009 02:45:48 (+12:21:38):  
Bit error in QI lag 201 bit 9  
[#4339 I=-1 Q=1] 08-jul-2009 02:47:20 (+12:23:10):  
Bit error in II lag 49 bit 29  
Bit error in II lag 397 bit 18  
[#4340 I=-1 Q=0] 08-jul-2009 02:47:30 (+12:23:20):  
Bit error in QI lag 453 bit 14  
[#4342 I=-1 Q=-1] 08-jul-2009 02:47:50 (+12:23:40):  
Bit error in II lag 266 bit 20  
[#4356 I=0 Q=1] 08-jul-2009 02:50:14 (+12:26:4):  
Bit error in II lag 235 bit 14  
[#4357 I=-1 Q=-1] 08-jul-2009 02:50:26 (+12:26:16):  
Bit error in IQ lag 0 bit 30  
[#4359 I=-1 Q=1] 08-jul-2009 02:50:46 (+12:26:36):  
Bit error in II lag 104 bit 22  
Bit error in IQ lag 63 bit 23  
[#4363 I=1 Q=0] 08-jul-2009 02:51:26 (+12:27:16):  
Bit error in IQ lag 38 bit 24  
[#4364 I=1 Q=1] 08-jul-2009 02:51:38 (+12:27:28):  
Bit error in IQ lag 171 bit 24  
[#4371 I=0 Q=-1] 08-jul-2009 02:52:48 (+12:28:38):  
Bit error in IQ lag 56 bit 17  
[#4379 I=-1 Q=0] 08-jul-2009 02:54:12 (+12:30:2):  
Bit error in QQ lag 463 bit 28  
[#4384 I=-1 Q=-1] 08-jul-2009 02:55:02 (+12:30:52):  
Bit error in II lag 218 bit 12  
[#4388 I=-1 Q=-1] 08-jul-2009 02:55:44 (+12:31:34):  
Bit error in QI lag 183 bit 30  
[#4390 I=1 Q=-1] 08-jul-2009 02:56:04 (+12:31:54):  
Bit error in QQ lag 442 bit 15  
[#4391 I=-1 Q=1] 08-jul-2009 02:56:14 (+12:32:4):  
Bit error in IQ lag 498 bit 14  
[#4394 I=-1 Q=-1] 08-jul-2009 02:56:46 (+12:32:36):  
Bit error in QI lag 457 bit 13  
[#4398 I=0 Q=1] 08-jul-2009 02:57:26 (+12:33:16):  
Bit error in QI lag 413 bit 16  
[#4399 I=0 Q=-1] 08-jul-2009 02:57:36 (+12:33:26):  
Bit error in II lag 307 bit 26  
Bit error in IQ lag 85 bit 26  
[#4402 I=0 Q=-1] 08-jul-2009 02:58:08 (+12:33:58):  
Bit error in QI lag 19 bit 25  
[#4410 I=0 Q=1] 08-jul-2009 02:59:30 (+12:35:20):  
Bit error in QI lag 113 bit 19  
[#4414 I=0 Q=-1] 08-jul-2009 03:00:12 (+12:36:2):  
Bit error in II lag 438 bit 18  
[#4419 I=1 Q=-1] 08-jul-2009 03:01:02 (+12:36:52):  
Bit error in II lag 468 bit 6  
[#4422 I=-1 Q=1] 08-jul-2009 03:01:34 (+12:37:24):  
Bit error in QQ lag 474 bit 13  
[#4439 I=-1 Q=1] 08-jul-2009 03:04:28 (+12:40:18):  
Bit error in II lag 5 bit 26  
[#4447 I=1 Q=1] 08-jul-2009 03:05:50 (+12:41:40):  
Bit error in IQ lag 506 bit 25  
[#4448 I=1 Q=0] 08-jul-2009 03:06:00 (+12:41:50):  
Bit error in QI lag 39 bit 12  
[#4450 I=-1 Q=-1] 08-jul-2009 03:06:22 (+12:42:12):  
Bit error in IQ lag 397 bit 25  
[#4454 I=0 Q=-1] 08-jul-2009 03:07:02 (+12:42:52):  
Bit error in QQ lag 491 bit 18  
[#4457 I=1 Q=0] 08-jul-2009 03:07:34 (+12:43:24):  
Bit error in IQ lag 8 bit 29  
[#4458 I=1 Q=0] 08-jul-2009 03:07:44 (+12:43:34):

Bit error in QQ lag 71 bit 14  
[#4461 I=1 Q=1] 08-jul-2009 03:08:14 (+12:44:4):  
Bit error in II lag 252 bit 13  
Bit error in QQ lag 477 bit 17  
[#4466 I=-1 Q=-1] 08-jul-2009 03:09:06 (+12:44:56):  
Bit error in IQ lag 65 bit 22  
[#4477 I=1 Q=-1] 08-jul-2009 03:10:58 (+12:46:48):  
Bit error in QI lag 270 bit 16  
[#4478 I=0 Q=-1] 08-jul-2009 03:11:08 (+12:46:58):  
Bit error in II lag 492 bit 12  
[#4481 I=-1 Q=1] 08-jul-2009 03:11:40 (+12:47:30):  
Bit error in QQ lag 431 bit 24  
[#4484 I=1 Q=1] 08-jul-2009 03:12:10 (+12:47:60):  
Bit error in QQ lag 371 bit 20  
[#4489 I=0 Q=1] 08-jul-2009 03:13:02 (+12:48:52):  
Bit error in IQ lag 185 bit 16  
[#4494 I=1 Q=-1] 08-jul-2009 03:13:54 (+12:49:44):  
Bit error in II lag 300 bit 28  
[#4529 I=0 Q=0] 08-jul-2009 03:19:52 (+12:55:42):  
Bit error in QI lag 447 bit 23  
[#4553 I=0 Q=-1] 08-jul-2009 03:24:00 (+12:59:50):  
Bit error in IQ lag 228 bit 4  
[#4560 I=1 Q=-1] 08-jul-2009 03:25:12 (+13:1:2):  
Bit error in IQ lag 29 bit 25  
[#4562 I=0 Q=0] 08-jul-2009 03:25:32 (+13:1:22):  
Bit error in QI lag 489 bit 11  
[#4572 I=0 Q=0] 08-jul-2009 03:27:14 (+13:3:4):  
Bit error in IQ lag 282 bit 13  
[#4579 I=1 Q=-1] 08-jul-2009 03:28:26 (+13:4:16):  
Bit error in QQ lag 366 bit 19  
[#4587 I=1 Q=1] 08-jul-2009 03:29:48 (+13:5:38):  
Bit error in QQ lag 45 bit 5  
[#4602 I=-1 Q=0] 08-jul-2009 03:32:22 (+13:8:12):  
Bit error in IQ lag 272 bit 5  
[#4603 I=1 Q=0] 08-jul-2009 03:32:34 (+13:8:24):  
Bit error in IQ lag 93 bit 27  
[#4612 I=0 Q=1] 08-jul-2009 03:34:06 (+13:9:56):  
Bit error in II lag 457 bit 13  
[#4617 I=-1 Q=1] 08-jul-2009 03:34:56 (+13:10:46):  
Bit error in II lag 501 bit 24  
[#4622 I=1 Q=-1] 08-jul-2009 03:35:48 (+13:11:38):  
Bit error in II lag 506 bit 13  
[#4624 I=0 Q=-1] 08-jul-2009 03:36:08 (+13:11:58):  
Bit error in QQ lag 247 bit 28  
[#4627 I=-1 Q=0] 08-jul-2009 03:36:40 (+13:12:30):  
Bit error in QQ lag 53 bit 15  
[#4630 I=-1 Q=-1] 08-jul-2009 03:37:10 (+13:13:0):  
Bit error in IQ lag 504 bit 12  
Bit error in QI lag 47 bit 6  
[#4632 I=0 Q=1] 08-jul-2009 03:37:32 (+13:13:22):  
Bit error in IQ lag 239 bit 24  
[#4633 I=1 Q=-1] 08-jul-2009 03:37:42 (+13:13:32):  
Bit error in QI lag 495 bit 15  
[#4643 I=0 Q=1] 08-jul-2009 03:39:24 (+13:15:14):  
Bit error in II lag 68 bit 10  
[#4645 I=0 Q=0] 08-jul-2009 03:39:44 (+13:15:34):  
Bit error in IQ lag 476 bit 5  
[#4647 I=0 Q=0] 08-jul-2009 03:40:06 (+13:15:56):  
Bit error in II lag 415 bit 19  
[#4652 I=1 Q=1] 08-jul-2009 03:40:56 (+13:16:46):  
Bit error in II lag 3 bit 18  
[#4654 I=-1 Q=0] 08-jul-2009 03:41:18 (+13:17:8):  
Bit error in QI lag 329 bit 28  
[#4657 I=1 Q=-1] 08-jul-2009 03:41:48 (+13:17:38):  
Bit error in II lag 374 bit 18  
[#4662 I=-1 Q=-1] 08-jul-2009 03:42:40 (+13:18:30):  
Bit error in II lag 504 bit 14  
[#4667 I=-1 Q=-1] 08-jul-2009 03:43:30 (+13:19:20):  
Bit error in QI lag 249 bit 28  
[#4692 I=1 Q=0] 08-jul-2009 03:47:48 (+13:23:38):  
Bit error in QI lag 405 bit 11  
[#4694 I=0 Q=-1] 08-jul-2009 03:48:08 (+13:23:58):  
Bit error in II lag 438 bit 30  
[#4700 I=0 Q=0] 08-jul-2009 03:49:10 (+13:24:60):  
Bit error in II lag 502 bit 13  
[#4716 I=1 Q=0] 08-jul-2009 03:51:54 (+13:27:44):  
Bit error in IQ lag 284 bit 12  
[#4726 I=-1 Q=1] 08-jul-2009 03:53:36 (+13:29:26):  
Bit error in IQ lag 96 bit 18  
[#4733 I=0 Q=1] 08-jul-2009 03:54:48 (+13:30:38):  
Bit error in IQ lag 32 bit 30  
[#4745 I=0 Q=0] 08-jul-2009 03:56:52 (+13:32:42):  
Bit error in II lag 261 bit 12  
[#4750 I=-1 Q=-1] 08-jul-2009 03:57:44 (+13:33:34):



Bit error in QI lag 286 bit 5  
Bit error in QQ lag 417 bit 27  
[#4752 I=0 Q=0] 08-jul-2009 03:58:04 (+13:33:54):  
Bit error in QQ lag 284 bit 16  
[#4757 I=-1 Q=1] 08-jul-2009 03:58:56 (+13:34:46):  
Bit error in IQ lag 83 bit 25  
Bit error in QQ lag 18 bit 4  
[#4763 I=-1 Q=0] 08-jul-2009 03:59:56 (+13:35:46):  
Bit error in QI lag 417 bit 24  
[#4791 I=1 Q=1] 08-jul-2009 04:04:44 (+13:40:34):  
Bit error in IQ lag 271 bit 26  
[#4797 I=-1 Q=0] 08-jul-2009 04:05:46 (+13:41:36):  
Bit error in II lag 16 bit 14  
[#4808 I=-1 Q=0] 08-jul-2009 04:07:40 (+13:43:30):  
Bit error in QQ lag 367 bit 27  
[#4809 I=0 Q=1] 08-jul-2009 04:07:50 (+13:43:40):  
Bit error in QI lag 330 bit 27  
[#4818 I=1 Q=-1] 08-jul-2009 04:09:22 (+13:45:12):  
Bit error in QQ lag 439 bit 3  
[#4831 I=1 Q=1] 08-jul-2009 04:11:36 (+13:47:26):  
Bit error in II lag 35 bit 18  
[#4833 I=0 Q=1] 08-jul-2009 04:11:56 (+13:47:46):  
Bit error in IQ lag 31 bit 17  
[#4835 I=-1 Q=1] 08-jul-2009 04:12:16 (+13:48:6):  
Bit error in QQ lag 58 bit 15  
[#4841 I=0 Q=1] 08-jul-2009 04:13:18 (+13:49:8):  
Bit error in IQ lag 272 bit 5  
[#4842 I=-1 Q=-1] 08-jul-2009 04:13:28 (+13:49:18):  
Bit error in II lag 16 bit 24  
[#4844 I=-1 Q=-1] 08-jul-2009 04:13:50 (+13:49:40):  
Bit error in QI lag 53 bit 15  
[#4846 I=1 Q=-1] 08-jul-2009 04:14:10 (+13:49:60):  
Bit error in QQ lag 14 bit 28  
[#4856 I=0 Q=0] 08-jul-2009 04:15:52 (+13:51:42):  
Bit error in QI lag 335 bit 27  
[#4857 I=0 Q=-1] 08-jul-2009 04:16:02 (+13:51:52):  
Bit error in QI lag 203 bit 10  
[#4865 I=0 Q=1] 08-jul-2009 04:17:24 (+13:53:14):  
Bit error in QI lag 39 bit 19  
[#4871 I=1 Q=-1] 08-jul-2009 04:18:26 (+13:54:16):  
Bit error in IQ lag 88 bit 18  
Bit error in QI lag 240 bit 12  
Bit error in QQ lag 209 bit 12  
[#4874 I=0 Q=1] 08-jul-2009 04:18:58 (+13:54:48):  
Bit error in IQ lag 45 bit 30  
[#4889 I=-1 Q=1] 08-jul-2009 04:21:32 (+13:57:22):  
Bit error in QI lag 412 bit 11  
[#4890 I=-1 Q=-1] 08-jul-2009 04:21:42 (+13:57:32):  
Bit error in QI lag 311 bit 2  
[#4893 I=0 Q=-1] 08-jul-2009 04:22:12 (+13:58:2):  
Bit error in II lag 255 bit 13  
[#4898 I=1 Q=-1] 08-jul-2009 04:23:04 (+13:58:54):  
Bit error in II lag 441 bit 12  
[#4899 I=0 Q=1] 08-jul-2009 04:23:14 (+13:59:4):  
Bit error in QQ lag 473 bit 18  
[#4904 I=0 Q=0] 08-jul-2009 04:24:06 (+13:59:56):  
Bit error in IQ lag 117 bit 14  
[#4905 I=0 Q=1] 08-jul-2009 04:24:16 (+14:0:6):  
Bit error in QQ lag 3 bit 24  
[#4909 I=1 Q=1] 08-jul-2009 04:24:56 (+14:0:46):  
Bit error in QI lag 374 bit 15  
[#4922 I=-1 Q=1] 08-jul-2009 04:27:10 (+14:2:60):  
Bit error in QI lag 72 bit 12  
[#4939 I=1 Q=0] 08-jul-2009 04:30:04 (+14:5:54):  
Bit error in IQ lag 32 bit 4  
[#4940 I=0 Q=-1] 08-jul-2009 04:30:16 (+14:6:6):  
Bit error in IQ lag 221 bit 14  
[#4952 I=0 Q=0] 08-jul-2009 04:32:18 (+14:8:8):  
Bit error in QI lag 51 bit 15  
[#4957 I=-1 Q=-1] 08-jul-2009 04:33:10 (+14:8:60):  
Bit error in QI lag 286 bit 5  
Bit error in QI lag 492 bit 12  
[#4962 I=0 Q=-1] 08-jul-2009 04:34:02 (+14:9:52):  
Bit error in QI lag 260 bit 24  
[#4966 I=-1 Q=1] 08-jul-2009 04:34:42 (+14:10:32):  
Bit error in IQ lag 135 bit 4  
Bit error in QI lag 467 bit 30  
[#4982 I=-1 Q=-1] 08-jul-2009 04:37:26 (+14:13:16):  
Bit error in II lag 506 bit 12  
Bit error in QQ lag 498 bit 28  
[#4984 I=1 Q=-1] 08-jul-2009 04:37:48 (+14:13:38):  
Bit error in QQ lag 0 bit 4  
[#4990 I=0 Q=0] 08-jul-2009 04:38:48 (+14:14:38):  
Bit error in II lag 57 bit 30

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[#5000 I=1 Q=1] 08-jul-2009 04:40:32 (+14:16:22):  
Bit error in II lag 453 bit 25  
[#5011 I=-1 Q=0] 08-jul-2009 04:42:24 (+14:18:14):  
Bit error in II lag 208 bit 15  
[#5012 I=1 Q=-1] 08-jul-2009 04:42:34 (+14:18:24):  
Bit error in II lag 90 bit 26  
[#5013 I=1 Q=-1] 08-jul-2009 04:42:46 (+14:18:36):  
Bit error in IQ lag 273 bit 28  
[#5015 I=1 Q=-1] 08-jul-2009 04:43:06 (+14:18:56):  
Bit error in IQ lag 484 bit 30  
[#5016 I=-1 Q=-1] 08-jul-2009 04:43:16 (+14:19:6):  
Bit error in II lag 215 bit 26  
Bit error in QQ lag 289 bit 11  
[#5025 I=0 Q=1] 08-jul-2009 04:44:48 (+14:20:38):  
Bit error in IQ lag 501 bit 18  
[#5046 I=-1 Q=0] 08-jul-2009 04:48:24 (+14:24:14):  
Bit error in QQ lag 342 bit 13  
[#5048 I=1 Q=1] 08-jul-2009 04:48:44 (+14:24:34):  
Bit error in IQ lag 278 bit 2  
[#5053 I=0 Q=1] 08-jul-2009 04:49:36 (+14:25:26):  
Bit error in II lag 509 bit 24  
[#5057 I=0 Q=1] 08-jul-2009 04:50:18 (+14:26:8):  
Bit error in IQ lag 119 bit 6  
[#5065 I=1 Q=1] 08-jul-2009 04:51:40 (+14:27:30):  
Bit error in QQ lag 94 bit 28  
[#5067 I=1 Q=1] 08-jul-2009 04:52:00 (+14:27:50):  
Bit error in II lag 236 bit 8  
[#5072 I=-1 Q=-1] 08-jul-2009 04:52:52 (+14:28:42):  
Bit error in IQ lag 467 bit 12  
[#5093 I=1 Q=1] 08-jul-2009 04:56:28 (+14:32:18):  
Bit error in II lag 308 bit 29  
[#5097 I=-1 Q=-1] 08-jul-2009 04:57:08 (+14:32:58):  
Bit error in IQ lag 114 bit 21  
Bit error in QI lag 74 bit 28  
[#5111 I=-1 Q=-1] 08-jul-2009 04:59:32 (+14:35:22):  
Bit error in IQ lag 225 bit 30  
[#5120 I=0 Q=-1] 08-jul-2009 05:01:04 (+14:36:54):  
Bit error in IQ lag 117 bit 1  
[#5122 I=0 Q=-1] 08-jul-2009 05:01:26 (+14:37:16):  
Bit error in IQ lag 248 bit 12  
[#5123 I=0 Q=0] 08-jul-2009 05:01:36 (+14:37:26):  
Bit error in IQ lag 312 bit 30  
[#5129 I=-1 Q=0] 08-jul-2009 05:02:38 (+14:38:28):  
Bit error in II lag 272 bit 16  
[#5131 I=-1 Q=1] 08-jul-2009 05:02:58 (+14:38:48):  
Bit error in II lag 374 bit 11  
Bit error in QI lag 478 bit 26  
[#5144 I=0 Q=0] 08-jul-2009 05:05:12 (+14:41:2):  
Bit error in QI lag 42 bit 19  
[#5159 I=-1 Q=0] 08-jul-2009 05:07:46 (+14:43:36):  
Bit error in IQ lag 210 bit 18  
[#5161 I=1 Q=-1] 08-jul-2009 05:08:06 (+14:43:56):  
Bit error in IQ lag 277 bit 13  
[#5169 I=1 Q=1] 08-jul-2009 05:09:28 (+14:45:18):  
Bit error in QI lag 313 bit 24  
[#5186 I=-1 Q=1] 08-jul-2009 05:12:22 (+14:48:12):  
Bit error in QQ lag 479 bit 30  
[#5191 I=-1 Q=-1] 08-jul-2009 05:13:14 (+14:49:4):  
Bit error in II lag 266 bit 5  
[#5198 I=-1 Q=1] 08-jul-2009 05:14:26 (+14:50:16):  
Bit error in IQ lag 223 bit 24  
[#5204 I=0 Q=1] 08-jul-2009 05:15:28 (+14:51:18):  
Bit error in II lag 168 bit 27  
[#5212 I=-1 Q=0] 08-jul-2009 05:16:50 (+14:52:40):  
Bit error in QI lag 274 bit 4  
[#5213 I=0 Q=-1] 08-jul-2009 05:17:00 (+14:52:50):  
Bit error in QI lag 3 bit 15  
[#5226 I=1 Q=-1] 08-jul-2009 05:19:14 (+14:55:4):  
Bit error in QI lag 471 bit 24  
[#5227 I=1 Q=1] 08-jul-2009 05:19:24 (+14:55:14):  
Bit error in II lag 234 bit 26  
[#5236 I=-1 Q=-1] 08-jul-2009 05:20:56 (+14:56:46):  
Bit error in QQ lag 118 bit 28  
[#5237 I=1 Q=-1] 08-jul-2009 05:21:08 (+14:56:58):  
Bit error in IQ lag 267 bit 28  
[#5241 I=0 Q=-1] 08-jul-2009 05:21:48 (+14:57:38):  
Bit error in QQ lag 214 bit 25  
[#5249 I=1 Q=1] 08-jul-2009 05:23:10 (+14:58:60):  
Bit error in QI lag 12 bit 5  
[#5259 I=0 Q=1] 08-jul-2009 05:24:54 (+15:0:44):  
Bit error in IQ lag 79 bit 27  
[#5277 I=1 Q=-1] 08-jul-2009 05:27:58 (+15:3:48):  
Bit error in QQ lag 360 bit 19  
[#5283 I=0 Q=1] 08-jul-2009 05:29:00 (+15:4:50):
```

Bit error in QI lag 502 bit 24  
[#5308 I=0 Q=-1] 08-jul-2009 05:33:16 (+15:9:6):  
Bit error in II lag 284 bit 26  
Bit error in QI lag 97 bit 9  
[#5313 I=-1 Q=0] 08-jul-2009 05:34:08 (+15:9:58):  
Bit error in II lag 21 bit 28  
Bit error in QQ lag 142 bit 22  
[#5319 I=1 Q=1] 08-jul-2009 05:35:10 (+15:11:0):  
Bit error in IQ lag 331 bit 28  
[#5322 I=0 Q=1] 08-jul-2009 05:35:40 (+15:11:30):  
Bit error in IQ lag 324 bit 30  
[#5326 I=0 Q=-1] 08-jul-2009 05:36:22 (+15:12:12):  
Bit error in IQ lag 470 bit 24  
[#5328 I=1 Q=-1] 08-jul-2009 05:36:42 (+15:12:32):  
Bit error in IQ lag 291 bit 13  
[#5330 I=1 Q=-1] 08-jul-2009 05:37:02 (+15:12:52):  
Bit error in II lag 255 bit 12  
Bit error in IQ lag 251 bit 18  
[#5341 I=0 Q=1] 08-jul-2009 05:38:56 (+15:14:46):  
Bit error in II lag 261 bit 29  
[#5342 I=1 Q=0] 08-jul-2009 05:39:06 (+15:14:56):  
Bit error in QI lag 241 bit 30  
[#5353 I=1 Q=-1] 08-jul-2009 05:41:00 (+15:16:50):  
Bit error in II lag 507 bit 28  
Bit error in IQ lag 65 bit 27  
[#5365 I=0 Q=1] 08-jul-2009 05:43:02 (+15:18:52):  
Bit error in II lag 250 bit 17  
[#5366 I=0 Q=0] 08-jul-2009 05:43:12 (+15:19:2):  
Bit error in II lag 299 bit 12  
[#5376 I=1 Q=1] 08-jul-2009 05:44:56 (+15:20:46):  
Bit error in QQ lag 446 bit 30  
[#5382 I=0 Q=1] 08-jul-2009 05:45:58 (+15:21:48):  
Bit error in QI lag 18 bit 28  
[#5383 I=1 Q=1] 08-jul-2009 05:46:08 (+15:21:58):  
Bit error in IQ lag 388 bit 19  
[#5385 I=-1 Q=1] 08-jul-2009 05:46:28 (+15:22:18):  
Bit error in IQ lag 1 bit 11  
[#5395 I=-1 Q=1] 08-jul-2009 05:48:10 (+15:23:60):  
Bit error in IQ lag 482 bit 16  
[#5415 I=0 Q=1] 08-jul-2009 05:51:36 (+15:27:26):  
Bit error in IQ lag 408 bit 18  
[#5427 I=1 Q=-1] 08-jul-2009 05:53:40 (+15:29:30):  
Bit error in QQ lag 384 bit 27  
[#5429 I=1 Q=0] 08-jul-2009 05:54:00 (+15:29:50):  
Bit error in QQ lag 288 bit 28  
[#5439 I=0 Q=-1] 08-jul-2009 05:55:42 (+15:31:32):  
Bit error in QI lag 227 bit 5  
[#5441 I=0 Q=-1] 08-jul-2009 05:56:04 (+15:31:54):  
Bit error in QQ lag 247 bit 13  
[#5442 I=1 Q=0] 08-jul-2009 05:56:14 (+15:32:4):  
Bit error in QI lag 18 bit 15  
[#5450 I=-1 Q=1] 08-jul-2009 05:57:36 (+15:33:26):  
Bit error in II lag 247 bit 6  
Bit error in II lag 484 bit 19  
Bit error in QQ lag 348 bit 11  
[#5451 I=0 Q=0] 08-jul-2009 05:57:46 (+15:33:36):  
Bit error in QQ lag 119 bit 22  
[#5460 I=1 Q=0] 08-jul-2009 05:59:18 (+15:35:8):  
Bit error in QI lag 503 bit 12  
[#5463 I=1 Q=1] 08-jul-2009 05:59:50 (+15:35:40):  
Bit error in IQ lag 274 bit 18  
[#5466 I=0 Q=-1] 08-jul-2009 06:00:20 (+15:36:10):  
Bit error in II lag 507 bit 4  
[#5470 I=-1 Q=1] 08-jul-2009 06:01:02 (+15:36:52):  
Bit error in QQ lag 270 bit 5  
[#5478 I=1 Q=1] 08-jul-2009 06:02:24 (+15:38:14):  
Bit error in II lag 502 bit 18  
[#5485 I=1 Q=1] 08-jul-2009 06:03:36 (+15:39:26):  
Bit error in II lag 481 bit 26  
[#5487 I=1 Q=1] 08-jul-2009 06:03:56 (+15:39:46):  
Bit error in QI lag 229 bit 29  
[#5496 I=1 Q=1] 08-jul-2009 06:05:30 (+15:41:20):  
Bit error in II lag 96 bit 1  
[#5497 I=0 Q=1] 08-jul-2009 06:05:40 (+15:41:30):  
Bit error in QI lag 129 bit 15  
[#5504 I=1 Q=1] 08-jul-2009 06:06:52 (+15:42:42):  
Bit error in IQ lag 24 bit 25  
Bit error in IQ lag 499 bit 13  
[#5507 I=-1 Q=0] 08-jul-2009 06:07:22 (+15:43:12):  
Bit error in II lag 240 bit 24  
[#5508 I=1 Q=0] 08-jul-2009 06:07:32 (+15:43:22):  
Bit error in QI lag 6 bit 17  
[#5514 I=1 Q=0] 08-jul-2009 06:08:34 (+15:44:24):  
Bit error in QI lag 435 bit 5

[#5517 I=-1 Q=-1] 08-jul-2009 06:09:06 (+15:44:56):  
Bit error in IQ lag 477 bit 12  
Bit error in QI lag 450 bit 17  
[#5518 I=1 Q=1] 08-jul-2009 06:09:16 (+15:45:6):  
Bit error in II lag 40 bit 5  
[#5522 I=1 Q=1] 08-jul-2009 06:09:56 (+15:45:46):  
Bit error in QQ lag 489 bit 17  
[#5532 I=-1 Q=1] 08-jul-2009 06:11:40 (+15:47:30):  
Bit error in QQ lag 464 bit 11  
[#5538 I=0 Q=0] 08-jul-2009 06:12:42 (+15:48:32):  
Bit error in IQ lag 13 bit 29  
[#5546 I=0 Q=1] 08-jul-2009 06:14:04 (+15:49:54):  
Bit error in QI lag 180 bit 25  
[#5550 I=1 Q=1] 08-jul-2009 06:14:46 (+15:50:36):  
Bit error in IQ lag 275 bit 17  
[#5561 I=0 Q=-1] 08-jul-2009 06:16:38 (+15:52:28):  
Bit error in QI lag 465 bit 25  
[#5564 I=1 Q=1] 08-jul-2009 06:17:08 (+15:52:58):  
Bit error in QQ lag 481 bit 5  
[#5576 I=-1 Q=0] 08-jul-2009 06:19:12 (+15:55:2):  
Bit error in II lag 454 bit 24  
[#5578 I=-1 Q=1] 08-jul-2009 06:19:32 (+15:55:22):  
Bit error in II lag 297 bit 18  
[#5585 I=1 Q=1] 08-jul-2009 06:20:44 (+15:56:34):  
Bit error in QI lag 507 bit 13  
[#5587 I=0 Q=-1] 08-jul-2009 06:21:06 (+15:56:56):  
Bit error in QI lag 249 bit 4  
[#5588 I=-1 Q=0] 08-jul-2009 06:21:16 (+15:57:6):  
Bit error in IQ lag 487 bit 18  
[#5596 I=1 Q=1] 08-jul-2009 06:22:38 (+15:58:28):  
Bit error in IQ lag 259 bit 13  
[#5613 I=1 Q=-1] 08-jul-2009 06:25:32 (+16:1:22):  
Bit error in QQ lag 152 bit 24  
Bit error in QQ lag 467 bit 28  
[#5616 I=0 Q=-1] 08-jul-2009 06:26:04 (+16:1:54):  
Bit error in IQ lag 248 bit 13  
[#5640 I=0 Q=-1] 08-jul-2009 06:30:10 (+16:5:60):  
Bit error in QI lag 359 bit 27  
[#5642 I=1 Q=0] 08-jul-2009 06:30:32 (+16:6:22):  
Bit error in IQ lag 302 bit 13  
[#5643 I=-1 Q=1] 08-jul-2009 06:30:42 (+16:6:32):  
Bit error in IQ lag 224 bit 4  
[#5646 I=-1 Q=-1] 08-jul-2009 06:31:12 (+16:7:2):  
Bit error in QQ lag 260 bit 29  
[#5657 I=1 Q=-1] 08-jul-2009 06:33:06 (+16:8:56):  
Bit error in II lag 228 bit 15  
Bit error in IQ lag 508 bit 6  
[#5679 I=0 Q=-1] 08-jul-2009 06:36:52 (+16:12:42):  
Bit error in II lag 506 bit 25  
[#5683 I=-1 Q=0] 08-jul-2009 06:37:32 (+16:13:22):  
Bit error in IQ lag 217 bit 25  
[#5697 I=-1 Q=1] 08-jul-2009 06:39:56 (+16:15:46):  
Bit error in QQ lag 223 bit 16  
[#5702 I=0 Q=1] 08-jul-2009 06:40:48 (+16:16:38):  
Bit error in II lag 247 bit 6  
Bit error in II lag 482 bit 28  
[#5715 I=-1 Q=1] 08-jul-2009 06:43:02 (+16:18:52):  
Bit error in QQ lag 443 bit 30  
[#5725 I=-1 Q=-1] 08-jul-2009 06:44:44 (+16:20:34):  
Bit error in IQ lag 30 bit 1  
[#5726 I=-1 Q=-1] 08-jul-2009 06:44:54 (+16:20:44):  
Bit error in QI lag 340 bit 29  
[#5734 I=-1 Q=0] 08-jul-2009 06:46:18 (+16:22:8):  
Bit error in II lag 181 bit 15  
[#5735 I=-1 Q=-1] 08-jul-2009 06:46:28 (+16:22:18):  
Bit error in QQ lag 461 bit 18  
[#5736 I=1 Q=-1] 08-jul-2009 06:46:38 (+16:22:28):  
Bit error in QI lag 230 bit 5  
[#5738 I=0 Q=1] 08-jul-2009 06:46:58 (+16:22:48):  
Bit error in IQ lag 83 bit 23  
[#5739 I=1 Q=-1] 08-jul-2009 06:47:08 (+16:22:58):  
Bit error in QQ lag 451 bit 29  
[#5740 I=1 Q=-1] 08-jul-2009 06:47:20 (+16:23:10):  
Bit error in IQ lag 356 bit 4  
[#5757 I=0 Q=1] 08-jul-2009 06:50:14 (+16:26:4):  
Bit error in QI lag 193 bit 13  
[#5771 I=1 Q=0] 08-jul-2009 06:52:38 (+16:28:28):  
Bit error in QQ lag 247 bit 16  
[#5772 I=0 Q=1] 08-jul-2009 06:52:48 (+16:28:38):  
Bit error in II lag 500 bit 12  
[#5784 I=-1 Q=-1] 08-jul-2009 06:54:52 (+16:30:42):  
Bit error in II lag 224 bit 26  
[#5788 I=1 Q=1] 08-jul-2009 06:55:32 (+16:31:22):  
Bit error in QI lag 279 bit 16

[#5791 I=-1 Q=0] 08-jul-2009 06:56:04 (+16:31:54):  
Bit error in QI lag 485 bit 1  
[#5796 I=0 Q=-1] 08-jul-2009 06:56:56 (+16:32:46):  
Bit error in IQ lag 203 bit 25  
[#5807 I=0 Q=1] 08-jul-2009 06:58:48 (+16:34:38):  
Bit error in II lag 69 bit 22  
[#5810 I=-1 Q=-1] 08-jul-2009 06:59:20 (+16:35:10):  
Bit error in QI lag 264 bit 24  
[#5820 I=-1 Q=1] 08-jul-2009 07:01:02 (+16:36:52):  
Bit error in QQ lag 246 bit 2  
[#5838 I=0 Q=0] 08-jul-2009 07:04:08 (+16:39:58):  
Bit error in IQ lag 211 bit 12  
[#5840 I=1 Q=0] 08-jul-2009 07:04:28 (+16:40:18):  
Bit error in QI lag 270 bit 24  
[#5848 I=-1 Q=-1] 08-jul-2009 07:05:50 (+16:41:40):  
Bit error in IQ lag 211 bit 25  
[#5854 I=0 Q=1] 08-jul-2009 07:06:52 (+16:42:42):  
Bit error in IQ lag 224 bit 26  
[#5859 I=1 Q=0] 08-jul-2009 07:07:42 (+16:43:32):  
Bit error in IQ lag 86 bit 12  
[#5868 I=0 Q=0] 08-jul-2009 07:09:16 (+16:45:6):  
Bit error in IQ lag 17 bit 24  
[#5878 I=0 Q=-1] 08-jul-2009 07:10:58 (+16:46:48):  
Bit error in II lag 504 bit 16  
[#5881 I=0 Q=1] 08-jul-2009 07:11:28 (+16:47:18):  
Bit error in IQ lag 268 bit 14  
[#5894 I=1 Q=0] 08-jul-2009 07:13:42 (+16:49:32):  
Bit error in IQ lag 327 bit 12  
[#5895 I=0 Q=1] 08-jul-2009 07:13:52 (+16:49:42):  
Bit error in QI lag 511 bit 12  
[#5900 I=0 Q=-1] 08-jul-2009 07:14:44 (+16:50:34):  
Bit error in II lag 153 bit 18  
Bit error in QQ lag 238 bit 5  
[#5904 I=1 Q=1] 08-jul-2009 07:15:26 (+16:51:16):  
Bit error in IQ lag 298 bit 24  
[#5909 I=0 Q=-1] 08-jul-2009 07:16:16 (+16:52:6):  
Bit error in QI lag 490 bit 25  
[#5912 I=-1 Q=0] 08-jul-2009 07:16:48 (+16:52:38):  
Bit error in QI lag 504 bit 1  
[#5914 I=1 Q=-1] 08-jul-2009 07:17:08 (+16:52:58):  
Bit error in QI lag 168 bit 1  
Bit error in QI lag 313 bit 6  
Bit error in QQ lag 440 bit 14  
[#5917 I=0 Q=-1] 08-jul-2009 07:17:38 (+16:53:28):  
Bit error in IQ lag 497 bit 17  
[#5918 I=1 Q=-1] 08-jul-2009 07:17:50 (+16:53:40):  
Bit error in QQ lag 507 bit 12  
[#5919 I=-1 Q=0] 08-jul-2009 07:18:00 (+16:53:50):  
Bit error in IQ lag 325 bit 11  
[#5921 I=0 Q=0] 08-jul-2009 07:18:20 (+16:54:10):  
Bit error in II lag 24 bit 12  
[#5945 I=1 Q=1] 08-jul-2009 07:22:26 (+16:58:16):  
Bit error in QI lag 257 bit 30  
Bit error in QI lag 437 bit 28  
[#5949 I=0 Q=-1] 08-jul-2009 07:23:08 (+16:58:58):  
Bit error in II lag 494 bit 24  
[#5955 I=-1 Q=0] 08-jul-2009 07:24:10 (+16:59:60):  
Bit error in IQ lag 183 bit 17  
[#5956 I=-1 Q=-1] 08-jul-2009 07:24:20 (+17:0:10):  
Bit error in II lag 40 bit 17  
Bit error in QI lag 264 bit 5  
[#5962 I=-1 Q=-1] 08-jul-2009 07:25:22 (+17:1:12):  
Bit error in QI lag 162 bit 10  
[#5965 I=0 Q=1] 08-jul-2009 07:25:52 (+17:1:42):  
Bit error in II lag 506 bit 12  
[#5975 I=0 Q=1] 08-jul-2009 07:27:34 (+17:3:24):  
Bit error in II lag 221 bit 4  
Bit error in QI lag 266 bit 11  
[#5981 I=-1 Q=1] 08-jul-2009 07:28:36 (+17:4:26):  
Bit error in QI lag 229 bit 24  
[#5982 I=-1 Q=-1] 08-jul-2009 07:28:46 (+17:4:36):  
Bit error in QI lag 23 bit 15  
[#5987 I=-1 Q=-1] 08-jul-2009 07:29:38 (+17:5:28):  
Bit error in QQ lag 455 bit 4  
Bit error in QQ lag 501 bit 17  
[#5994 I=0 Q=0] 08-jul-2009 07:30:50 (+17:6:40):  
Bit error in QI lag 497 bit 30  
[#5997 I=1 Q=-1] 08-jul-2009 07:31:22 (+17:7:12):  
Bit error in QQ lag 263 bit 16  
[#5999 I=-1 Q=1] 08-jul-2009 07:31:42 (+17:7:32):  
Bit error in QI lag 75 bit 8  
Bit error in QQ lag 498 bit 17  
[#6001 I=1 Q=0] 08-jul-2009 07:32:02 (+17:7:52):  
Bit error in QI lag 44 bit 30

[#6022 I=-1 Q=1] 08-jul-2009 07:35:38 (+17:11:28):  
Bit error in II lag 508 bit 4  
[#6033 I=-1 Q=-1] 08-jul-2009 07:37:32 (+17:13:22):  
Bit error in QQ lag 466 bit 18  
[#6035 I=-1 Q=0] 08-jul-2009 07:37:52 (+17:13:42):  
Bit error in IQ lag 315 bit 25  
[#6037 I=-1 Q=0] 08-jul-2009 07:38:12 (+17:14:2):  
Bit error in QI lag 67 bit 28  
[#6042 I=1 Q=1] 08-jul-2009 07:39:04 (+17:14:54):  
Bit error in IQ lag 164 bit 27  
[#6046 I=1 Q=-1] 08-jul-2009 07:39:44 (+17:15:34):  
Bit error in II lag 379 bit 28  
[#6047 I=0 Q=0] 08-jul-2009 07:39:54 (+17:15:44):  
Bit error in IQ lag 498 bit 17  
[#6054 I=0 Q=1] 08-jul-2009 07:41:06 (+17:16:56):  
Bit error in IQ lag 254 bit 5  
[#6061 I=1 Q=1] 08-jul-2009 07:42:18 (+17:18:8):  
Bit error in QQ lag 14 bit 13  
[#6068 I=0 Q=-1] 08-jul-2009 07:43:30 (+17:19:20):  
Bit error in QQ lag 440 bit 25  
[#6076 I=0 Q=0] 08-jul-2009 07:44:52 (+17:20:42):  
Bit error in IQ lag 497 bit 25  
[#6091 I=0 Q=0] 08-jul-2009 07:47:26 (+17:23:16):  
Bit error in IQ lag 334 bit 11  
[#6101 I=-1 Q=-1] 08-jul-2009 07:49:10 (+17:24:60):  
Bit error in IQ lag 0 bit 30  
[#6114 I=-1 Q=-1] 08-jul-2009 07:51:24 (+17:27:14):  
Bit error in IQ lag 20 bit 25  
[#6115 I=0 Q=0] 08-jul-2009 07:51:34 (+17:27:24):  
Bit error in II lag 471 bit 12  
Bit error in QI lag 237 bit 28  
[#6120 I=0 Q=-1] 08-jul-2009 07:52:24 (+17:28:14):  
Bit error in II lag 244 bit 18  
Bit error in IQ lag 273 bit 28  
[#6121 I=1 Q=0] 08-jul-2009 07:52:36 (+17:28:26):  
Bit error in IQ lag 487 bit 26  
[#6136 I=-1 Q=-1] 08-jul-2009 07:55:10 (+17:30:60):  
Bit error in IQ lag 389 bit 27  
[#6145 I=0 Q=1] 08-jul-2009 07:56:42 (+17:32:32):  
Bit error in IQ lag 279 bit 12  
[#6150 I=-1 Q=-1] 08-jul-2009 07:57:32 (+17:33:22):  
Bit error in QI lag 100 bit 14  
[#6157 I=-1 Q=1] 08-jul-2009 07:58:44 (+17:34:34):  
Bit error in II lag 459 bit 26  
[#6158 I=0 Q=0] 08-jul-2009 07:58:56 (+17:34:46):  
Bit error in IQ lag 135 bit 16  
Bit error in QQ lag 174 bit 25  
[#6165 I=1 Q=-1] 08-jul-2009 08:00:08 (+17:35:58):  
Bit error in QI lag 1 bit 29  
[#6166 I=-1 Q=1] 08-jul-2009 08:00:18 (+17:36:8):  
Bit error in II lag 438 bit 18  
[#6169 I=-1 Q=-1] 08-jul-2009 08:00:48 (+17:36:38):  
Bit error in QI lag 250 bit 5  
[#6196 I=1 Q=1] 08-jul-2009 08:05:26 (+17:41:16):  
Bit error in II lag 489 bit 6  
[#6200 I=0 Q=-1] 08-jul-2009 08:06:06 (+17:41:56):  
Bit error in QQ lag 274 bit 4  
[#6201 I=0 Q=1] 08-jul-2009 08:06:16 (+17:42:6):  
Bit error in QI lag 171 bit 14  
[#6212 I=-1 Q=-1] 08-jul-2009 08:08:10 (+17:43:60):  
Bit error in II lag 503 bit 6  
[#6222 I=-1 Q=-1] 08-jul-2009 08:09:52 (+17:45:42):  
Bit error in II lag 507 bit 12  
[#6223 I=-1 Q=-1] 08-jul-2009 08:10:02 (+17:45:52):  
Bit error in IQ lag 308 bit 14  
Bit error in IQ lag 467 bit 18  
[#6228 I=-1 Q=1] 08-jul-2009 08:10:54 (+17:46:44):  
Bit error in QQ lag 209 bit 4  
[#6230 I=0 Q=0] 08-jul-2009 08:11:14 (+17:47:4):  
Bit error in QQ lag 230 bit 14  
[#6250 I=-1 Q=1] 08-jul-2009 08:14:40 (+17:50:30):  
Bit error in IQ lag 51 bit 13  
[#6254 I=-1 Q=1] 08-jul-2009 08:15:22 (+17:51:12):  
Bit error in IQ lag 467 bit 29  
[#6255 I=1 Q=1] 08-jul-2009 08:15:32 (+17:51:22):  
Bit error in QI lag 506 bit 17  
[#6261 I=-1 Q=-1] 08-jul-2009 08:16:34 (+17:52:24):  
Bit error in QI lag 471 bit 13  
[#6269 I=0 Q=1] 08-jul-2009 08:17:56 (+17:53:46):  
Bit error in II lag 419 bit 19  
[#6273 I=-1 Q=0] 08-jul-2009 08:18:36 (+17:54:26):  
Bit error in IQ lag 476 bit 14  
[#6279 I=1 Q=-1] 08-jul-2009 08:19:38 (+17:55:28):  
Bit error in QQ lag 273 bit 18

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[#6286 I=-1 Q=1] 08-jul-2009 08:20:50 (+17:56:40):  
Bit error in QI lag 195 bit 22  
[#6294 I=0 Q=1] 08-jul-2009 08:22:12 (+17:58:2):  
Bit error in IQ lag 354 bit 1  
[#6304 I=-1 Q=1] 08-jul-2009 08:23:54 (+17:59:44):  
Bit error in QQ lag 296 bit 17  
[#6312 I=-1 Q=-1] 08-jul-2009 08:25:16 (+18:1:6):  
Bit error in QI lag 388 bit 26  
[#6324 I=-1 Q=1] 08-jul-2009 08:27:20 (+18:3:10):  
Bit error in II lag 312 bit 4  
[#6333 I=1 Q=-1] 08-jul-2009 08:28:52 (+18:4:42):  
Bit error in IQ lag 295 bit 18  
[#6345 I=1 Q=1] 08-jul-2009 08:30:56 (+18:6:46):  
Bit error in QQ lag 265 bit 16  
[#6358 I=0 Q=0] 08-jul-2009 08:33:10 (+18:8:60):  
Bit error in II lag 489 bit 26  
[#6359 I=-1 Q=1] 08-jul-2009 08:33:20 (+18:9:10):  
Bit error in II lag 314 bit 18  
[#6370 I=1 Q=0] 08-jul-2009 08:35:12 (+18:11:2):  
Bit error in QI lag 270 bit 29  
[#6374 I=0 Q=1] 08-jul-2009 08:35:54 (+18:11:44):  
Bit error in QI lag 258 bit 30  
[#6375 I=1 Q=1] 08-jul-2009 08:36:04 (+18:11:54):  
Bit error in QQ lag 179 bit 10  
[#6379 I=0 Q=1] 08-jul-2009 08:36:46 (+18:12:36):  
Bit error in QI lag 491 bit 30  
[#6386 I=0 Q=-1] 08-jul-2009 08:37:58 (+18:13:48):  
Bit error in QQ lag 184 bit 18  
[#6392 I=-1 Q=-1] 08-jul-2009 08:38:58 (+18:14:48):  
Bit error in IQ lag 21 bit 24  
[#6397 I=0 Q=1] 08-jul-2009 08:39:50 (+18:15:40):  
Bit error in IQ lag 288 bit 14  
[#6408 I=0 Q=-1] 08-jul-2009 08:41:44 (+18:17:34):  
Bit error in IQ lag 251 bit 12  
Bit error in QQ lag 276 bit 20  
[#6418 I=-1 Q=0] 08-jul-2009 08:43:26 (+18:19:16):  
Bit error in II lag 449 bit 26  
[#6420 I=0 Q=-1] 08-jul-2009 08:43:46 (+18:19:36):  
Bit error in IQ lag 292 bit 24  
[#6432 I=0 Q=1] 08-jul-2009 08:45:50 (+18:21:40):  
Bit error in QI lag 4 bit 19  
[#6434 I=-1 Q=0] 08-jul-2009 08:46:10 (+18:21:60):  
Bit error in IQ lag 49 bit 23  
[#6441 I=1 Q=0] 08-jul-2009 08:47:22 (+18:23:12):  
Bit error in IQ lag 502 bit 29  
[#6444 I=0 Q=-1] 08-jul-2009 08:47:52 (+18:23:42):  
Bit error in II lag 32 bit 16  
Bit error in IQ lag 393 bit 26  
[#6445 I=-1 Q=-1] 08-jul-2009 08:48:04 (+18:23:54):  
Bit error in II lag 443 bit 29  
Bit error in II lag 479 bit 24  
[#6451 I=-1 Q=-1] 08-jul-2009 08:49:04 (+18:24:54):  
Bit error in QI lag 16 bit 7  
[#6452 I=1 Q=1] 08-jul-2009 08:49:16 (+18:25:6):  
Bit error in QI lag 126 bit 27  
[#6461 I=-1 Q=0] 08-jul-2009 08:50:48 (+18:26:38):  
Bit error in IQ lag 384 bit 20  
[#6462 I=0 Q=1] 08-jul-2009 08:50:58 (+18:26:48):  
Bit error in IQ lag 10 bit 13  
[#6468 I=0 Q=-1] 08-jul-2009 08:52:00 (+18:27:50):  
Bit error in IQ lag 274 bit 24  
[#6469 I=1 Q=0] 08-jul-2009 08:52:10 (+18:28:0):  
Bit error in QI lag 149 bit 22  
[#6470 I=0 Q=1] 08-jul-2009 08:52:20 (+18:28:10):  
Bit error in QI lag 239 bit 13  
[#6474 I=1 Q=1] 08-jul-2009 08:53:02 (+18:28:52):  
Bit error in IQ lag 505 bit 30  
Bit error in QQ lag 98 bit 20  
[#6475 I=0 Q=-1] 08-jul-2009 08:53:12 (+18:29:2):  
Bit error in QI lag 289 bit 4  
[#6476 I=0 Q=0] 08-jul-2009 08:53:22 (+18:29:12):  
Bit error in QQ lag 50 bit 1  
[#6480 I=1 Q=-1] 08-jul-2009 08:54:04 (+18:29:54):  
Bit error in QQ lag 20 bit 7  
Bit error in QQ lag 491 bit 17  
[#6491 I=1 Q=-1] 08-jul-2009 08:56:00 (+18:31:50):  
Bit error in IQ lag 165 bit 16  
[#6498 I=-1 Q=1] 08-jul-2009 08:57:12 (+18:33:2):  
Bit error in IQ lag 437 bit 6  
[#6501 I=-1 Q=1] 08-jul-2009 08:57:42 (+18:33:32):  
Bit error in IQ lag 259 bit 12  
[#6502 I=1 Q=0] 08-jul-2009 08:57:54 (+18:33:44):  
Bit error in IQ lag 488 bit 28  
[#6509 I=0 Q=1] 08-jul-2009 08:59:06 (+18:34:56):
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Bit error in IQ lag 473 bit 17  
[#6518 I=-1 Q=1] 08-jul-2009 09:00:38 (+18:36:28):  
Bit error in QI lag 244 bit 28  
[#6541 I=0 Q=1] 08-jul-2009 09:04:34 (+18:40:24):  
Bit error in QQ lag 273 bit 30  
[#6542 I=-1 Q=-1] 08-jul-2009 09:04:44 (+18:40:34):  
Bit error in QI lag 203 bit 4  
[#6544 I=1 Q=-1] 08-jul-2009 09:05:04 (+18:40:54):  
Bit error in IQ lag 139 bit 9  
[#6546 I=1 Q=0] 08-jul-2009 09:05:26 (+18:41:16):  
Bit error in QQ lag 249 bit 11

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